September 2, 2026

# QuickLogic Unveils Next-Generation eFPGA Hard IP for GlobalFoundries 12LP

*- New release delivers improved performance and area efficiency, scales beyond 250K LUTs, and adds enhanced DSP and optional configuration bit health monitoring*

SAN JOSE, Calif., Sept. 2, 2026 /PRNewswire/ -- The enhanced eFPGA Hard IP builds on QuickLogic's silicon-proven technology for GF 12LP and expands its capabilities for compute-intensive and high-reliability applications. Key new features include:



- **Enhanced signal processing:** A new DSP Multiply-and-Accumulate (MAC) unit, with enhanced preadder, cascade connections, and SIMD functionality, provides greater support for phased array radar, electronic warfare, wireless infrastructure, satellite communications, and imaging algorithms.
- **Scalability beyond 250K LUTs:** The eFPGA fabric can scale to support more than 250,000 LUTs, addressing designs with larger embedded programmable logic requirements.
- **Optional Configuration Bit Health Monitor:** Detects and corrects configuration bit errors induced by Single Event Upsets (SEUs) while the eFPGA user design is active, supporting applications where near-real-time configuration integrity and system reliability are paramount.

"Customers increasingly want the flexibility to adapt hardware functions after tape-out without sacrificing the performance, power and area advantages of an ASIC," said Andy Jaros, VP of IP Sales at QuickLogic. "Our enhanced eFPGA Hard IP for GF 12LP gives SoC developers greater scalability and signal-processing capability, while our optional Configuration Bit Health Monitor adds another level of reliability for the most demanding applications."

QuickLogic's eFPGA Hard IP enables programmable logic to be embedded directly within an SoC, allowing critical hardware functions, algorithms and interfaces to be updated post-manufacturing. The architecture can be scaled and configured with user-defined combinations of LUT, BlockRAM and DSP resources to address a range of applications, with complete RTL-to-bitstream development support through QuickLogic's Aurora™ and AuroraPRO™ tools.

**Learn More at GTS North America**

QuickLogic is a Silver Sponsor of GlobalFoundries Technology Summit (GTS) North America. Attendees can meet with the QuickLogic team to learn more about the enhanced

GF 12LP eFPGA Hard IP and QuickLogic's embedded FPGA solutions.

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| What: | GlobalFoundries Technology Summit (GTS) North America |
| Location: | Santa Clara Marriott in Santa Clara, CA |
| Booth: | #11 in the Partner Pavilion |
| Date: | September 2, 2026 |

For more information about QuickLogic eFPGA Hard IP, visitwww.quicklogic.com.

**About QuickLogic**

QuickLogic is a fabless semiconductor company specializing in embedded FPGA (eFPGA) Hard IP, Strategic Radiation Hardened and Antifuse FPGAs, and ruggedized programmable logic solutions. QuickLogic's unique approach combines cutting-edge technology with open-source tools to deliver highly customizable low-power solutions for aerospace and defense, industrial, computing, and consumer markets. For more information, visit www.quicklogic.com.

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