November 12, 2024

AMD Announces Versal Premium Series Gen 2 Enabling New Levels of System Acceleration to Meet the Demands of Data-Intensive Workloads

Extends Versal Gen 2 portfolio with industry's first FPGA devices featuring CXL 3.1 and PCIe Gen6, with LPDDR5X support, for fast connectivity, more efficient data movement, and unlocking more memory

SANTA CLARA, Calif., Nov. 12, 2024 (GLOBE NEWSWIRE) -- AMD (NASDAQ: AMD) today announced the AMD Versal™ Premium Series Gen 2, an adaptive SoC platform designed to deliver the highest levels of system acceleration for a wide range of workloads. Versal Premium Series Gen 2 will be the FPGA industry's first devices featuring Compute Express Link (CXL®) 3.1 and PCIe® Gen6 as well as LPDDR5X memory support in hard IP.[1]

These next-generation interface and memory technologies access and move data rapidly and efficiently between processors and accelerators. CXL 3.1 and LPDDR5X help unlock more memory resources faster to address the growing real-time processing and storage demands of data-intensive applications in data center, communications, test and measurement, and aerospace and defense markets.

"System architects are constantly looking to pack more data into smaller spaces and move data more efficiently between parts of the system," said Salil Raje, senior vice president and general manager, Adaptive and Embedded Computing Group, AMD. "Our latest addition to the Versal Gen 2 portfolio helps customers improve overall system throughput and utilization of memory resources to achieve the highest performance and unlock insights for their most demanding applications from the cloud to the edge."

Accelerate Host Connectivity
AMD champions open innovation through its support of CXL, an open industry-standard interconnect between processors and devices such as FPGA-based accelerators. With support for CXL 3.1 and PCIe Gen6, the industry's fastest host interfaces,[2] Versal Premium Gen 2 devices enable industry-leading, high-bandwidth host CPU-to-accelerator connectivity. PCIe Gen6 offers a 2-4X faster line rate compared to competing FPGAs with PCIe Gen4 or Gen5 support,[2] while CXL 3.1 running PCIe Gen6 provides double the bandwidth of competing devices with CXL 2.1[3] at similar latencies, as well as enhanced fabric and coherency capabilities.

Additionally, by pairing Versal Premium Series Gen 2 with an AMD EPYC™ CPUs, system architects can leverage the latest AMD FPGA-based device connected via CXL or PCIe to a high-performance CPU, to accelerate data-intensive applications and meet rapid data

growth demands. CXL also brings an additional benefit of memory coherency to help enable true heterogeneous, accelerated computing.

Increasing Memory Bandwidth and Utilization
AMD Versal Premium Series Gen 2 adaptive SoCs accelerate memory bandwidth for faster data transfers and real-time responsiveness with the fastest LPDDR5X memory connectivity available, at up to 8533 Mb/s. This ultra-fast, enhanced DDR memory enables up to 2.7X faster host connectivity over comparable competitive devices with LPDDR4/5 memory.[4]

Connectivity to CXL memory expansion modules enable up to 2.7X more total bandwidth than LPDDR5X memory alone.[5] As a result, the Versal Premium Series Gen 2 allows for scalable memory pooling and extension for multiple accelerators, optimizing memory utilization and increasing bandwidth and capacity.

By dynamically allocating a memory pool for multiple devices, Versal Premium Series Gen 2 adaptive SoCs are designed to improve memory utilization in a Multi-Headed Single Logic Device (MH-SLD), allowing it to operate without a fabric or switch, while supporting up to two CXL hosts.

Strengthen Data Security
Enhanced security features help the Versal Premium Series Gen 2 transfer data quickly and securely, both in transit and at rest. It is the industry's first FPGA device to feature support for integrated PCIe® Integrity and Data Encryption (IDE) in hard IP.[6] Inline encryption built into hard DDR memory controllers helps secure data at rest, while 400G High-Speed Crypto Engines help the device secure user data at up to 2X faster line rates, enabling faster secure data transactions.[7]

AMD Versal Premium Series Gen 2 development tools are expected to be available in Q2 2025, followed by the availability of silicon samples by early 2026. Production shipments are expected to begin in the second half of 2026.

Supporting Resources

- Learn more about the AMD Versal™ Premium Series Gen 2
- Learn more about the AMD Versal™ product portfolio
- Connect with AMD on LinkedIn
- Follow AMD on X

About AMD
For more than 50 years AMD has driven innovation in high-performance computing, graphics and visualization technologies. Billions of people, leading Fortune 500 businesses and cutting-edge scientific research institutions around the world rely on AMD technology daily to improve how they live, work and play. AMD employees are focused on building leadership high-performance and adaptive products that push the boundaries of what is possible. For more information about how AMD is enabling today and inspiring tomorrow, visit the AMD (NASDAQ: AMD) website, blog, LinkedIn, and X pages.

AMD, the AMD Arrow logo, EPYC, Versal, and combinations thereof are trademarks of Advanced Micro Devices, Inc. CXL is a trademark of Compute Express Link Consortium, Inc. PCI, PCIe, and PCI Express are trademarks of PCI-SIG and used under license. Other names are for informational purposes only and may be trademarks of their respective owners.

Media Contacts:
Mike Sanchez
AMD Communications
+1 209-262-7458
M.Sanchez@amd.com

Mitch Haws
AMD Investor Relations
+1 408-749-2845
Mitch.Haws@amd.com

---

[1] Based on an AMD internal analysis of AMD Versal Premium Series Gen 2 devices with CXL 3.1 and PCIe Gen6 vs. comparable competitive devices without CXL 3.1 and/or with PCIe Gen 4/5, as of July 2024. (VER-055)

[2] Based on an AMD internal analysis of the Versal Premium Series Gen 2 devices with PCIe Gen6 vs. comparable competitive and prior generation AMD Versal Premium Series devices with PCIe Gen4/5, as of July 2024. Actual line rate speeds will vary based on system configuration and other factors. (VER-057)

[3] Based on an AMD internal analysis of Versal Premium Series Gen 2 devices with CXL 3.1 vs. comparable competitive device(s) with CXL 2.0, as of July 2024. Actual line rate speed will vary based on system configuration and other factors. (VER-056)

[4] Based on an AMD internal analysis of Versal Premium Series Gen 2 device DDR/LPDDR memory interface specifications vs. comparable competitive devices, as of July 2024. Actual performance will vary based on system configuration and other factors. (VER-058)

[5] Based on an AMD internal analysis of the total memory bandwidth (CXL 3.1 and LPDDR5X memory components) available with Versal Premium Series Gen 2 devices vs. the same devices with LPDDR5X memory alone. Memory bandwidth will vary based on system configuration and other factors. (VER-059)

[6] Based on AMD internal analysis in October 2024, AMD Versal Premium Series Gen 2 devices include the PCIe Integrity and Data Encryption feature, while the competition does not. (VER-064)

[7] Based on AMD internal analysis of Versal Premium Series Gen 2 devices with 400 Gb/s high-speed crypto engines vs. comparable competitive devices with 200 Gb/s crypto engines. Actual line rate speeds will vary based on system configuration and other factors. (VER-062)

Source: Advanced Micro Devices, Inc.