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SST Announces Qualification of Embedded SuperFlash® on 110 nm CMOS Process

Industry's First ESF3 Technology on 110 nm Platform for Smart Card and MCU Applications

CHANDLER, Ariz., Feb. 15, 2017 (GLOBE NEWSWIRE) -- Microchip Technology Inc. (NASDAQ:MCHP), a leading provider of microcontroller, mixed-signal, analog and Flash-IP solutions, through its Silicon Storage Technology (SST) subsidiary, announced today qualification and availability of SST's third-generation embedded SuperFlash® (ESF3) non-volatile memory (NVM) on 110 nm Complementary Metal-Oxide-Semiconductor (CMOS) platform.

SST's embedded SuperFlash memory solution offers low power, high reliability, superior data retention and high endurance benefits to smart card, microcontroller (MCU) and other Flash-enabled specialized IC designers with a cost-effective, embedded Flash solution. In smart card applications, the fast erase time and low power offers a uniquely low energy envelope that is critical for enabling low-power applications such as Near Field Communication (NFC) and dual-interface smart cards.

For the first time SST's ESF3 technology is available to fabless and Integrated Device Manufacturers (IDMs) on a highly cost-effective 8-inch 110 nm CMOS platform. This ESF3 platform is qualified for 300,000 erase and program cycles, making it ideally suited for smart card and other high-endurance IC designs.

"The combination of low-power ESF3 technology on an advanced 110 nm process node opens up exciting new product opportunities, especially for the secure smart card market," said Vipin Tiwari, director of worldwide marketing and business development for SST. "Now customers who require low-power, high-endurance embedded Flash can keep production costs down by using this mainstream 8-inch CMOS platform."

Contact SST for more information on SST's extensive custom library of off-the-shelf IP blocks optimized for secure/smart card System-on-Chips (SoCs).

For more information on SST's patented and proprietary SuperFlash® NOR Flash technology visit www.sst.com/technology/SuperFlash-Overview.

About Silicon Storage Technology
Microchip Technology's SST subsidiary is a leading provider of embedded Flash technology. SST develops, designs, licenses and markets a diversified range of proprietary and patented SuperFlash memory technology solutions for the consumer, industrial, automotive and Internet of Things (IoT) markets. SST was founded in 1989, went public in 1995 (NASDAQ:SSTI), and was acquired by Microchip in April 2010. SST is now a wholly owned

subsidiary of Microchip, and is headquartered in San Jose, Calif. For more information, visit the SST website at www.sst.com.

About Microchip Technology
Microchip Technology Inc. (NASDAQ:MCHP) is a leading provider of microcontroller, mixed-signal, analog and Flash-IP solutions, providing low-risk product development, lower total system cost and faster time to market for thousands of diverse customer applications worldwide. Headquartered in Chandler, Arizona, Microchip offers outstanding technical support along with dependable delivery and quality. For more information, visit the Microchip website at www.microchip.com.

Note: The SST name, the SST logo and SuperFlash are registered trademarks of Microchip Technology Inc. and Silicon Storage Technology, Inc., as applicable, in the USA and other countries. All other trademarks mentioned herein are the property of their respective companies.

Editorial Contact:
Brian Thorsen
480-792-7182
brian.thorsen@microchip.com

Reader Inquiries:
1-888-624-7435

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