

June 18, 2025



QuickLogic to Exhibit at Chips to Systems Conference (DAC) 2025

SAN JOSE, Calif., June 18, 2025 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), a developer of embedded FPGA (eFPGA) Hard IP, ruggedized FPGAs, and endpoint AI solutions, today announced it will exhibit at the 2025 Chips to Systems Conference (DAC) in San Francisco.



Visit Booth #2222 to explore QuickLogic's advanced [eFPGA Hard IP](#) and chiplet solutions designed to help accelerate SoC and ASIC development and learn how QuickLogic's silicon-proven eFPGA Hard IP enables post-manufacturing flexibility, helping designers reduce time-to-market and cost while meeting performance, power, and area (PPA) goals. You will also discover our chiplet solutions, featuring validated, interoperable blocks backed by Universal PHY™ and a robust ecosystem—making heterogeneous integration faster, easier, and more efficient.

Event Details:

When: June 23–25, 2025

Where: Booth #2222 (Moscone West, 2nd Floor)

Website: www.dac.com

Come see us at DAC 2025 to learn how QuickLogic's eFPGA IP and chiplet-ready solutions can support your design goals with unmatched flexibility and efficiency.

About QuickLogic

QuickLogic Corporation is a fabless semiconductor company that specializes in eFPGA Hard IP, discrete FPGAs, and endpoint AI solutions. The company's unique technology, combined with open-source development tools, enables highly customizable and low-power silicon solutions for aerospace and defense, industrial, consumer, and edge computing markets. For more information, visit www.quicklogic.com.

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