

Microchip Expands Single Pair Ethernet Portfolio with New 10BASE-T1S Devices Enabling Scalable Edge Connectivity

New transceivers and integrated endpoint family simplify zonal architectures and reduce system complexity for automotive and industrial applications

CHANDLER, Ariz., Sept. 29, 2026 (GLOBE NEWSWIRE) -- Microchip Technology (**Nasdaq: MCHP**) today announces the expansion of its [10BASE-T1S portfolio](#) with the introduction of the LAN8679 and LAN8680 10BASE-T1S PMD transceivers, alongside the LAN8660X/1X/2X family of endpoints with integrated transceivers. Designed to support next-generation zonal architectures, these solutions deliver flexible, space-efficient connectivity that extends Ethernet to the network edge.

As automotive, industrial and robotics systems evolve toward fully Ethernet-based networks, designers face increasing demands for simplified architectures, reduced wiring complexity and efficient edge node integration. Microchip's latest 10BASE-T1S devices address these challenges and are designed to meet AEC-Q100 and ISO 26262 functional safety standards, building on Microchip's VelocityDRIVE™ portfolio with additional multidrop connectivity solutions featuring a standardized, streamlined interface.

The LAN8679 transceiver provides a standardized Open Alliance 3-Pin (OA3P) interface in a compact 8-pin VDFN package, supporting space-constrained edge nodes, while reducing pin count and design complexity. The LAN8680 builds on these capabilities by integrating System Basis Chip (SBC) features, including power management, system wake/sleep control and watchdog supervision. Both devices are engineered to enhance reliability and safety for demanding environments such as automotive and industrial applications.

"Microchip's latest 10BASE-T1S portfolio advances our commitment to enabling scalable, all-Ethernet network architectures," said Charlie Forni, corporate vice president of Microchip's networking and connectivity business unit. "By combining OA3P interface simplicity with integrated endpoint functionality, these devices give designers the flexibility to build efficient, reliable systems while significantly reducing complexity at the edge."

Complementing these devices, the LAN8660X/1X/2X endpoint family integrates a PMD transceiver into a single-chip Remote Control Protocol (RCP) solution. This approach enables designers to deploy compact, fully integrated edge nodes for applications such as control systems, lighting and audio without the need for external PHY components.

By leveraging 10BASE-T1S multidrop topology, Microchip's solutions allow multiple nodes—typically eight or more—to share a single bus line. This reduces wiring weight, lowers system

costs and eliminates the need for gateways, making it easier to connect sensors and actuators directly to the network edge.

Microchip's expanded Single Pair Ethernet portfolio targets a wide range of applications, including automotive zonal architecture, industrial automation, robotics and aerospace and defense systems. The products align with industry trends toward consolidated networks and simplified connectivity frameworks, enabling designers to implement efficient and scalable all-Ethernet architectures. For more information about Microchip's Single Pair Ethernet solutions, visit the [website](#).

Pricing and Availability

The LAN8679, LAN8680 and LAN8660X/1X/2X are available in limited sampling. To learn more, please [contact a Microchip sales representative](#).

Resources

High-res images available through Flickr or editorial contact (feel free to publish):

·Application image: www.flickr.com/photos/microchiptechnology/55399498402/sizes/l

About Microchip Technology:

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