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QuickLogic Collaborates with Mentor to Provide Seamless Design Environment for eFPGA Technology

- *QuickLogic Aurora development tools, together with Mentor's Precision® Synthesis, support complete RTL to bitstream design flow*
- *Tools optimized for QuickLogic ArcticPro architecture used in eFPGA IP*
- *Gives SoC developers seamless development environment for embedding FPGA logic*

SUNNYVALE, Calif., Dec. 14, 2017 (GLOBE NEWSWIRE) -- QuickLogic Corporation (NASDAQ:QUIK), a developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, display bridge and programmable logic solutions, announced that it has collaborated with Mentor®, a Siemens business, to provide a seamless design and development environment for its embedded FPGA (eFPGA) technology. Specifically, Mentor's Precision Synthesis software has been optimized to support the QuickLogic ArcticPro™ architecture used in the company's eFPGA IP.

QuickLogic will distribute this new version of Precision Synthesis as part of its Aurora™ development tool suite to provide high performance synthesis technology to eFPGA designers in their next SoC with embedded FPGA IP. The combination of the two tool sets will deliver a seamless development environment supporting a complete design flow, from RTL to programming bitstream, for the embedded FPGA portion of the design.

The tools from both companies have been tuned for implementation efficiency and design performance to enable the effective targeting of designs to the eFPGA IP. By embedding eFPGA technology, SoC developers gain post-manufacturing design flexibility to support design fixes, upgrades, market variants, and rapidly evolving standards or market requirements.

"We are pleased to collaborate with Mentor to give our customers complete design flow support for our eFPGA technology," said Mao Wang, director of product marketing at QuickLogic Corporation. "Mentor has done an excellent job in enabling their Precision Synthesis software to generate an optimized synthesis netlist for the QuickLogic ArcticPro-based eFPGA architecture."

"QuickLogic's eFPGA IP has the potential to be a transformative technology for our SoC customers, and we are looking forward to delivering an outstanding synthesis solution for their Aurora development tools and a continued growth in our partnership," said Ellie Burns, director of marketing, Calypto Systems Division at Mentor.

Availability
Mentor's Precision Synthesis and QuickLogic Aurora development tools supporting

QuickLogic's eFPGA technology are both available now from QuickLogic Corporation.

About QuickLogic
QuickLogic Corporation (NASDAQ:QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable, Hearable and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithm solutions for always-on voice and sensor processing. The company's embedded FPGA initiative also enables SoC designers to easily implement post production changes, and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, please visit www.quicklogic.com.

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