



QuickLogic Investor Presentation

NASDAQ: QUIK



Safe Harbor Statement

This press release contains forward-looking statements regarding our future business expectations, which are subject to the safe harbor provisions of the Private Securities Litigation Reform Act of 1995. These forward-looking statements are only predictions and may differ materially from actual results due to a variety of factors including: delays in the market acceptance of the Company's new products; the ability to convert design opportunities into customer revenue; our ability to replace revenue from end-of-life products; the level and timing of customer design activity; the market acceptance of our customers' products; the risk that new orders may not result in future revenue; our ability to introduce and produce new products based on advanced wafer technology on a timely basis; our ability to adequately market the low power, competitive pricing and short time-to-market of our new products; intense competition, including the introduction of new products by competitors; our ability to hire and retain qualified personnel; changes in product demand or supply; capacity constraints; and general economic conditions. These and other potential factors and uncertainties that could cause actual results to differ from the results predicted are described in more detail in the Company's public reports filed with the Securities and Exchange Commission (the "SEC"), including the risks discussed in the "Risk Factors" section in the Company's Annual Reports on Form 10-K, Quarterly Reports on Form 10-Q and in the Company's prior press releases, which are available on the Company's Investor Relations website at <http://ir.quicklogic.com/and> on the SEC website at www.sec.gov. QuickLogic expressly disclaims any obligation to update or revise any forward-looking statements found herein to reflect any changes in Company expectations or results or any change in events.

QuickLogic uses its website, the company blog QuickLogic HotSpot, corporate Twitter account, Facebook page, and LinkedIn page as channels of distribution of information about its products, its planned financial and other announcements, its attendance at upcoming investor and industry conferences, and other matters. Such information may be deemed material information, and QuickLogic may use these channels to comply with its disclosure obligations under Regulation FD.

Battery Life for Immersive User Experiences and Artificial Intelligence at the Endpoint

Smartphone, Wearable, Hearable & IoT devices, Always-on, Always-Listening



Artificial intelligence



Reacts accurately to voice commands



Proactively interprets user context



EOS™ S3

Always-on / Always-listening
Ultra-low power
Product design flexibility

ArcticPro™

eFPGA IP
eFPGA compiler
eFPGA place and route tools
Foundry partners



Contextually aware services



Longer battery life



User flexibility

Investment Rationale

<p>Addressing Critical Needs</p>	<ul style="list-style-type: none"> ▪ Enabling significantly longer battery life, immersive user experience & Artificial Intelligence ▪ Essential for Smartphones, Wearables, Hearables, Tablets, and IoT
<p>Tier 1 Customers & Ecosystem Partners</p>	
<p>Creating Competitive Advantages</p>	<ul style="list-style-type: none"> ▪ 50+ patents in core IP ▪ Delivers flexibility and reduced R&D costs for System on a Chip (SoC) vendors ▪ Enabling new form factors, new use cases ▪ Mobile-specific programmable logic enables Artificial Intelligence
<p>Validating Technology</p>	
<p>Driving to Profitable Growth</p>	<ul style="list-style-type: none"> ▪ Tier 1 Smartphone OEM Wearable; Several new design wins for Wearable, Hearable, Tablet, and IoT markets expected to ramp in 2H'2018 ▪ Entering new markets with eFPGA IP licensing opportunities ▪ Extending technology into Industrial IoT with Artificial Intelligence Initiative

Product Lines



Sensor Processing System-on-Chip

- Multi-core SoC for sensor processing, Endpoint Devices, Artificial Intelligence, immersive user experience of sensor fusion and always-on deeply embedded voice recognition



Ultra-Low Power Programmable Logic devices (FPGAs) and IP Licensing (eFPGA)

- Available as discrete devices, integrated into our multi-core SoCs, or available for IP licensing
- Enables pre-processing for Artificial Intelligence in Edge and Endpoint Devices

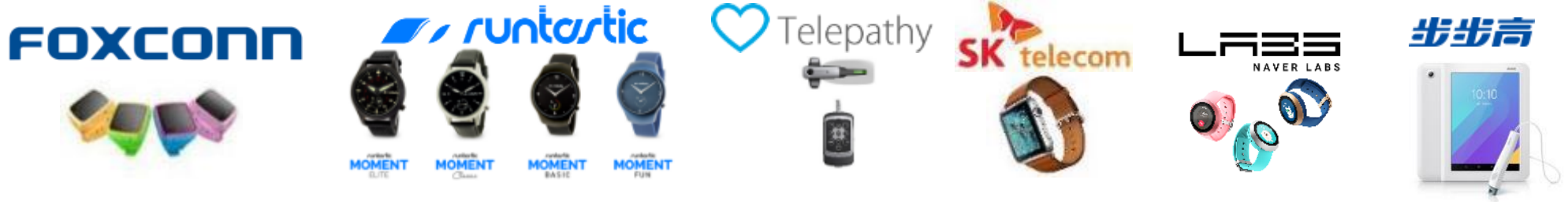


Display Bridges and Connectivity

- Solves interface mismatches between Application Processors and Displays
- MIPI DSI, RGB, and LVDS display support

Multiple Products & Markets

Sensor Processing



FPGA



Display Bridges





EOS™ S3

- Always-on / Always-listening
- Ultra-low power
- Product design flexibility

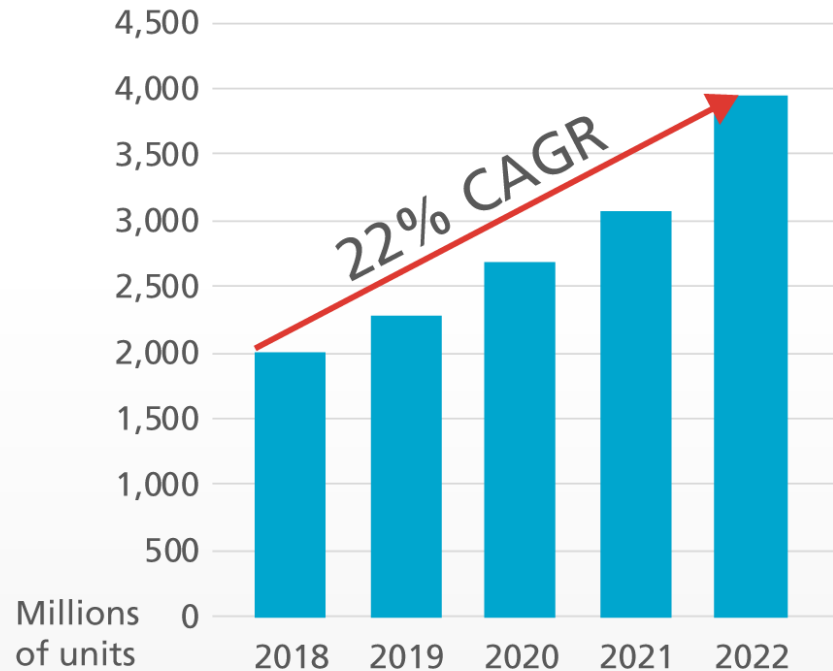
ArcticPro™

- eFPGA IP
- eFPGA compiler
- eFPGA place and route tools
- Foundry partners

Serving High Growth, High Volume Markets

1.5B Units in 2022

Discrete sensor hub market



Source: Daniel Associates, 2017

Market Drivers...

- More intelligence
- Contextually aware services
- Longer battery life
- **More computing at the edge**

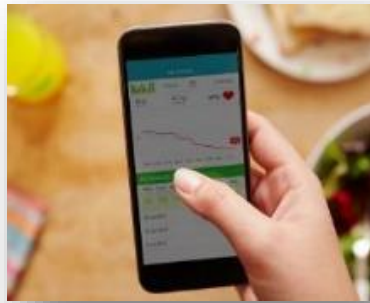
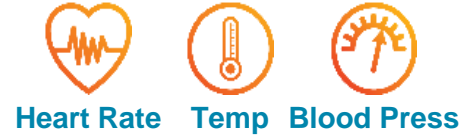


Immersive User Experience Requires More Sensors

Motion Sensors



Biological Sensors



Environmental Sensors



Gesture Sensors



Always-On / Always Listening Sensors Connect to the Cloud

Driven by Voice-Enabled Experience



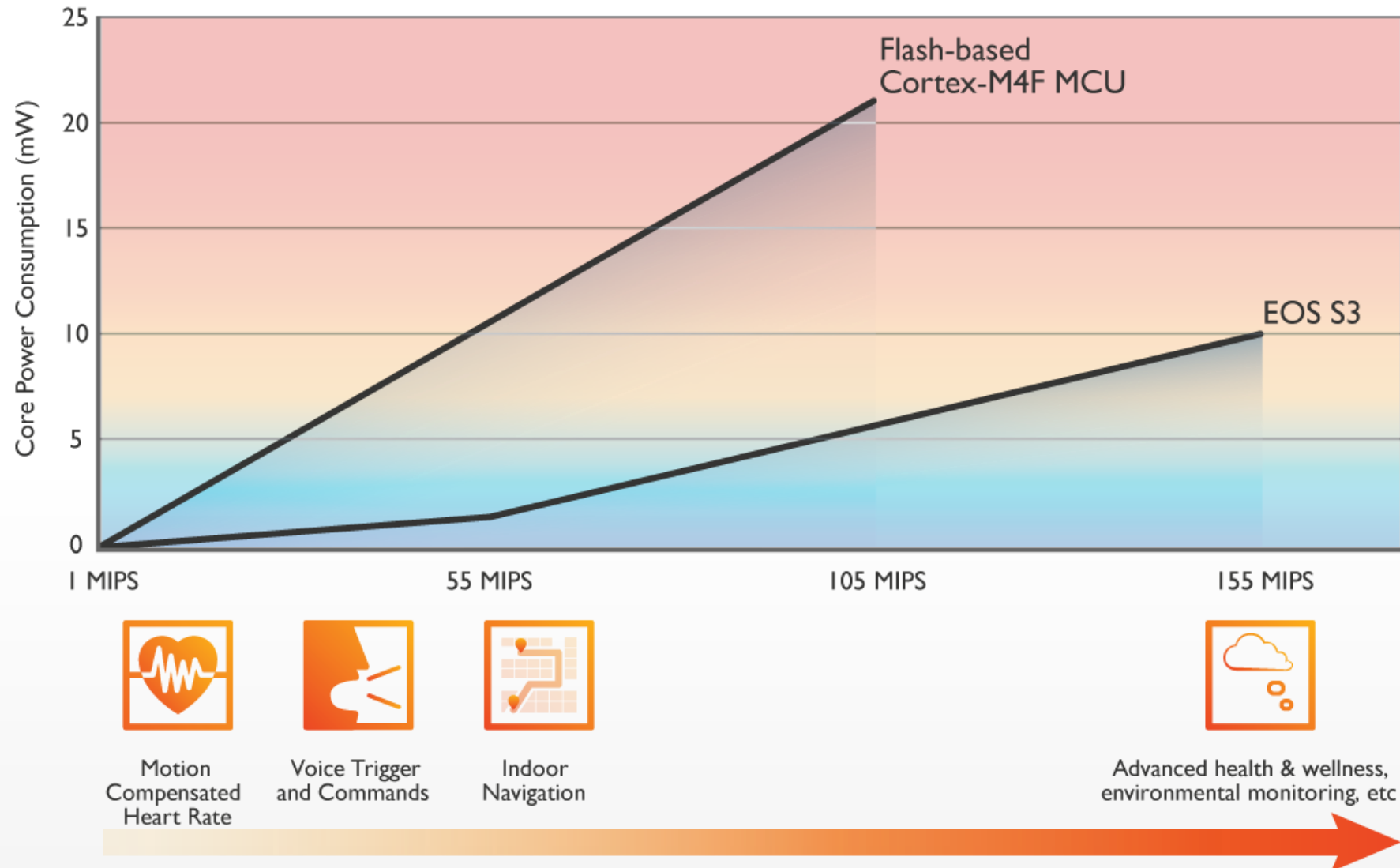
Voice-Enabled Artificial Intelligence

Focus of Both Large, and Emerging Technology Companies



CTO of a large hearable company called EOS S3 a
“masterpiece in system engineering”

More Immersive the Experience, the Greater Our Advantage



Our EOS™ S3 is Lower Power than All Other Architectures



Discrete
Sensor Hub

AP-Integrated
MCU/DSP

Sensor with Integrated
MCU

2018
Market Share

683Mu, 34% of TAM

869Mu, 44% of TAM

440Mu, 22% of TAM



QuickLogic is ½ the power of other discrete sensor hubs and 1/10th the power of AP-integrated MCU/DSP sensor hubs.

Source: Daniel Associates, 2017

Expanding Ecosystem Partnerships With Industry Leaders

Immersive User Experience & Artificial Intelligence at the Endpoint



Leading supplier of deeply embedded voice recognition technology



Global developer of embedded audio digital signal processing solutions



World leading vendor of Bluetooth® low energy Systems-on-Chip (SoCs)



The inventor of NeuroMem, a scalable neural network technology



Leading-edge software tools that enable the quick and easy generation of application-specific pattern recognition code.



Leading supplier of Android-compliant sensor fusion algorithms for Chinese OEMs



A total solution company for audio products



Rapidly growing leading enterprise provider of Artificial Intelligence (AI) speech technology



Produces the NM500, a NeuroMem-based IC that enables artificial & intelligence in endpoint solutions

Sensor Processing SoC Momentum

Ramping Design Wins Now

Wearables

- Naver Labs in Korea (\$20B market cap) – Smartwatch via Korea Telecom (KT)
- Tier 1 Smartphone OEM – Wearable design locked down, anticipate shipment in 2H'2018
- Two European OEMs – expect B2B Wearable and Fitness Wearable to ship in 2H'2018



Smartphones

- Japanese Smartphone OEM – anticipate Smartphone to market before year end

Tablets

- Large Chinese OEM – Design Win for tablet, expect to ship in Q3'2018



Hearables

- Cleer, Inc. selected EOS S3 for its EDGE Voice wireless headphones, a 2018 CES Innovation Awards honoree
- Multiple Amazon Alexa-enabled Hearable Design Wins shown at CES 2018 expect to ramp in 2H'2018

Ecosystem

- Murata introduced battery-powered WiFi Smart Speaker in May at Japan IoT Show
- Ultra-Low Power Amazon Alexa Support for Products Using Qualcomm Bluetooth Audio SoCs

Tier One Smartphone Wearable moving to productization stage with 3rd party software application companies. In addition, 2 high-volume consumer wearable and hearable opps targeted for mass production during 2018.

Embedded FPGA (eFPGA) IP Licensing



EOS™ S3

Always-on / Always-listening

Ultra-low power

Product design
flexibility

ArcticPro™

eFPGA IP

eFPGA compiler

eFPGA place and
route tools

Foundry partners

Entering New Markets

eFPGA IP Licensing Positioning

ArcticPro

- Targets significant financial potential
 - \$10M+ potential annual licensing revenue with upside on royalty revenue in 2+ years
- Creates new, very high gross margin manufacturing licenses revenue streams
- Delivers flexibility and reduced R&D costs for SoC vendors, enables Artificial Intelligence at Endpoint



The embedded semiconductor intellectual property (IP) market is expected to grow from \$3.09B to over \$7B by 2022, according to Markets&Markets.

eFPGA Benefits

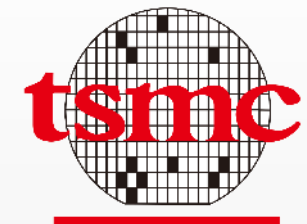
Adding Pre- and Post-Processing Capability to SoCs

- Enable Artificial Intelligence in Endpoint Applications
- Ultra-Low Power Consumption
- Increased Performance – Eliminates chip-to-chip delays
- Higher Revenues – Enables multiple product variants
- Greater Profits – Closer match to market needs
- Lower R&D Costs – Reduces development time & cost
- Faster Time to Market – Supports post-fabrication changes



Secured Two Top-tier eFPGA Foundries, Developing a Third

- ArcticPro™, **first** eFPGA announced for **new** GLOBALFOUNDRIES' 22FDX® (FD-SOI) process
- Supports 65nm and 40nm leveraging existing technology and foundry relationship with GLOBALFOUNDRIES
- Gained early access to GLOBALFOUNDRIES' 22nm FD-SOI Process by joining its FDXcelerator™ Partner Program
- Signed a semiconductor foundry manufacturing license agreement with SMIC for its 40nm process node
- Initiated new technology port TSMC

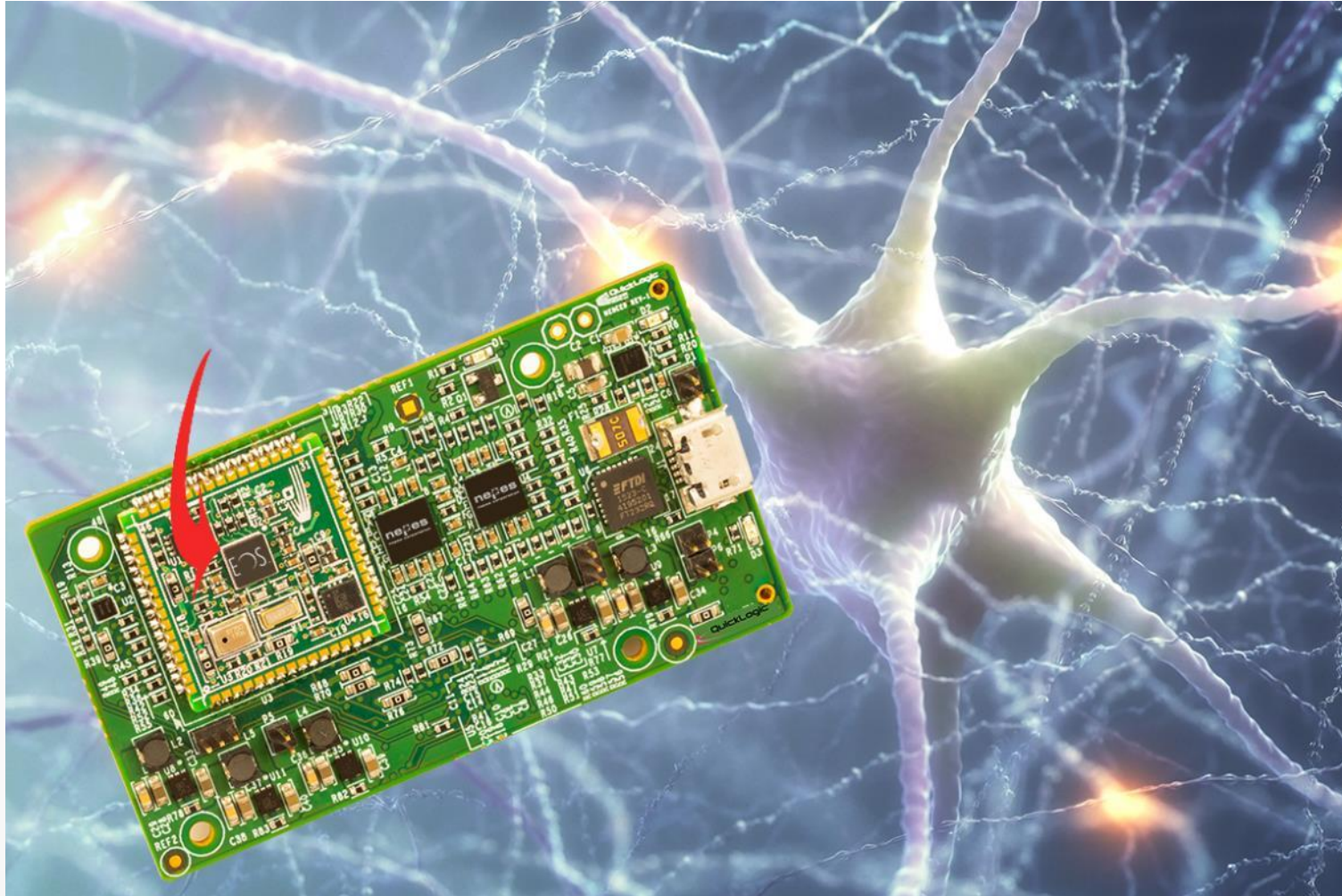


eFPGA – Market Momentum

- Joined RISC-V Foundation to broaden the adoption of open-source architecture and ecosystems and to leverage the eFPGA IP
- Partnered with Aldec to provide seamless simulation support for ArcticPro eFPGA technology
- Optimized Mentor's Precision Synthesis software to support ArcticPro™ architecture
- Partnered with AcconSys to expand ArcticPro eFPGA penetration in China
- Qualified ArcticPro™ eFPGA at SMIC to become the first eFPGA IP available on its low power 40nm LL process
- Established eFPGA IP Support Center in Taiwan to provide fast, local support for licensees, foundries and ecosystem partners
- Increased number of significant ArcticPro eFPGA engagements and expect to sign license agreements in 2H'2018



Future Growth - Artificial Intelligence for 350M unit IoT Market



Target Operating Model

Targets	Mid-Term (2 yr)	Long Term (>2 yr)
Revenue Growth	>50%	>50%
Gross Margin *	45% - 50%	>50%
Operating Inc *	0 - 10 %	10% - 15%

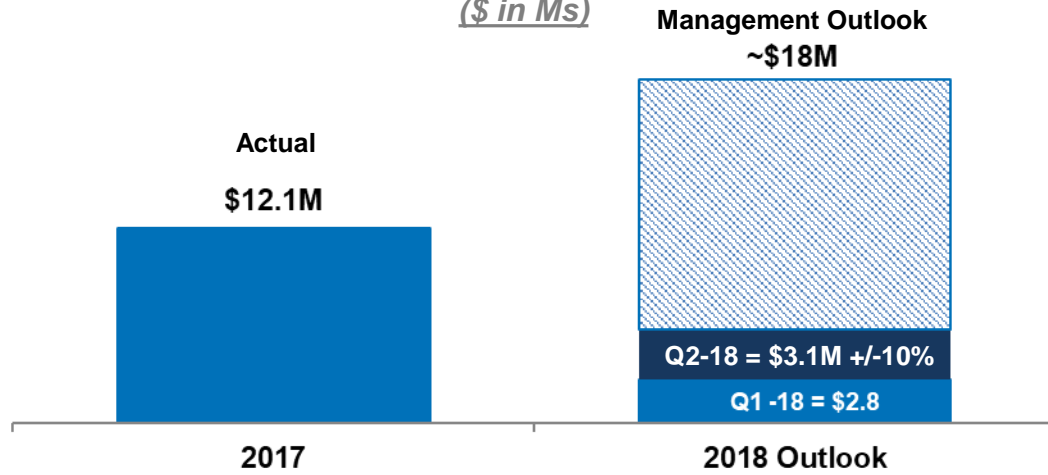
* Non-GAAP. See Appendix for GAAP reconciliation

NOTE: These projections are subject to a number of assumptions, risks, uncertainties and other factors that may cause our actual results to differ materially from such projections

Selected Income Statement Data

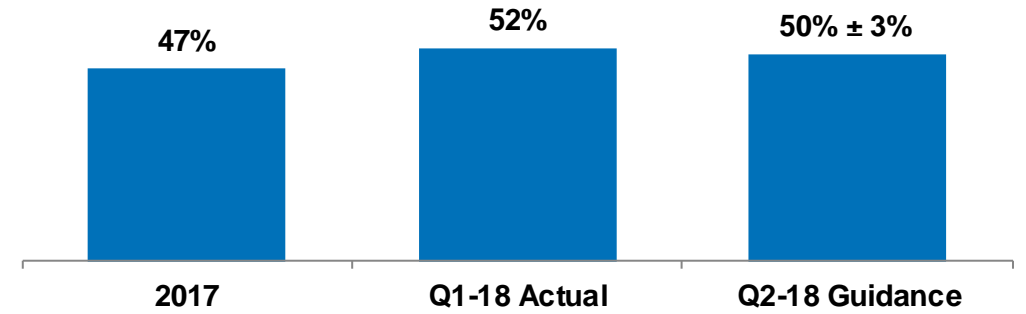
Revenue

(\$ in Ms)



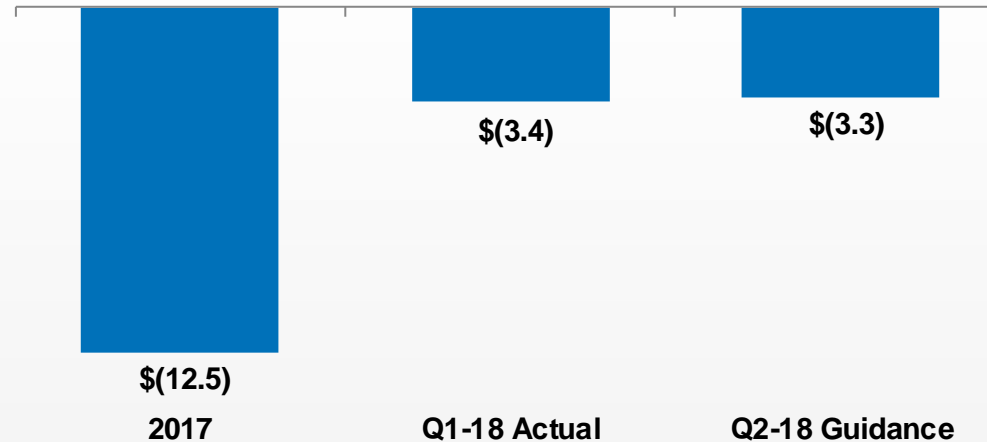
Gross Margin

(non-GAAP)*



Operating Loss

(non-GAAP)(\$ in Ms)*



* See Appendix for GAAP reconciliation

Selected Balance Sheet & Capitalization Data

Balance Sheet Data (Millions)	As of 4/1/18
Total Cash	\$12.6
Bank Debt - 4.63% Int.	\$6.0

Capitalization Table (as of 04/01/18)	
Common Stock Outstanding	80,627,561
Options Outstanding	3,558,121 (WAEP \$2.09)
RSUs Outstanding	3,165,618
Diluted Shares	87,351,300

Closed Secondary Offering on May 29, 2018 with estimated net proceeds of ~\$14M, 13,513,510 shares of common stock and 5,405,404 warrants.

Use of Proceeds: Strengthened Balance Sheet to better position us with large OEMs and suppliers, as well as continue funding strategic product and IP roadmaps.

Investment Rationale

<p>Addressing Critical Needs</p>	<ul style="list-style-type: none"> ▪ Enabling significantly longer battery life, immersive user experience & Artificial Intelligence ▪ Essential for Smartphones, Wearables, Hearables, Tablets, and IoT
<p>Tier 1 Customers & Ecosystem Partners</p>	
<p>Creating Competitive Advantages</p>	<ul style="list-style-type: none"> ▪ 50+ patents in core IP ▪ Delivers flexibility and reduced R&D costs for System on a Chip (SoC) vendors ▪ Enabling new form factors, new use cases ▪ Mobile-specific programmable logic enables Artificial Intelligence
<p>Validating Technology</p>	
<p>Driving to Profitable Growth</p>	<ul style="list-style-type: none"> ▪ Tier 1 Smartphone OEM Wearable; Several new design wins for Wearable, Hearable, Tablet, and IoT markets expected to ramp in 2H'2018 ▪ Entering new markets with eFPGA IP licensing opportunities ▪ Extending technology into Industrial IoT with Artificial Intelligence Initiative

Thank You!



Appendix



Non-GAAP Measures

QuickLogic reports financial information in accordance with GAAP, but believes that non-GAAP financial measures are helpful in evaluating its operating results and comparing its performance to comparable companies. Accordingly, the Company excludes charges related to stock-based compensation, restructuring, the effect of the write-off of long-lived assets and the tax effect on other comprehensive income in calculating non-GAAP (i) income (loss) from operations, (ii) net income (loss), (iii) net income (loss) per share, and (iv) gross margin percentage. The Company provides this non-GAAP information to enable investors to evaluate its operating results in a manner similar to how the Company analyzes its operating results and to provide consistency and comparability with similar companies in the Company's industry. Management uses the non-GAAP measures, which exclude gains, losses and other charges that are considered by management to be outside of the Company's core operating results, internally to evaluate its operating performance against results in prior periods and its operating plans and forecasts. In addition, the non-GAAP measures are used to plan for the Company's future periods and serve as a basis for the allocation of the Company's resources, management of operations and the measurement of profit-dependent cash and equity compensation paid to employees and executive officers. Investors should note, however, that the non-GAAP financial measures used by QuickLogic may not be the same non-GAAP financial measures, and may not be calculated in the same manner, as that of other companies. QuickLogic does not itself, nor does it suggest that investors should, consider such non-GAAP financial measures alone or as a substitute for financial information prepared in accordance with GAAP. A reconciliation of GAAP financial measures to non-GAAP financial measures is included in this Appendix to the presentation. Investors are encouraged to review the related GAAP financial measures and the reconciliation of non-GAAP financial measures with their most directly comparable GAAP financial measures.

P&L – Non-GAAP

Non-GAAP Results Millions (except for EPS)	Q1'17 Actual	Q2'17 Actual	Q3'17 Actual	Q4'17 Actual	FY 2017 Actual	Q1'18 Actual
New Product Revenue	\$1.9	\$1.5	\$1.5	\$1.0	\$5.9	\$1.3
Mature Revenue	\$1.3	\$1.5	\$1.5	\$2.0	\$6.3	\$1.5
Total Revenue	\$3.2	\$3.0	\$3.0	\$3.0	\$12.2	\$2.8
Gross Margin %	44%	46%	44%	52%	46%	52%
Research & Development	\$2.3	\$2.2	\$2.2	\$2.3	\$9.0	\$2.5
SG&A	\$2.3	\$2.4	\$2.1	\$2.3	\$9.1	\$2.3
Total Operating Expense	\$4.6	\$4.6	\$4.3	\$4.6	\$18.1	\$4.9
Operating Income (Loss)	(\$3.2)	(\$3.2)	(\$3.0)	(\$3.0)	(\$12.4)	(\$3.4)
Net Income (Loss)	(\$3.3)	(\$3.3)	(\$3.1)	(\$3.0)	(\$12.8)	(\$3.5)
EPS	(\$0.05)	(\$0.04)	(\$0.04)	(\$0.04)	(\$0.17)	(\$0.04)

GAAP to Non-GAAP Reconciliation

	Q1'17	Q2'17	Q3'17	Q4'17	Q1'18
(In \$ '000)	Actual	Actual	Actual	Actual	Actual
Non-GAAP Operating Loss	\$ (3,150)	\$ (3,206)	\$ (3,050)	\$ (3,091)	\$ (3,434)
Stock-based Compensation	(318)	(347)	(395)	(381)	(432)
Restructuring Costs	-	-	-	-	-
FA impairment and/or write-off	-	-	(10)	(2)	(5)
GAAP Operating Loss	\$ (3,468)	\$ (3,553)	\$ (3,455)	\$ (3,474)	\$ (3,871)

Non-GAAP Net Loss	\$ (3,247)	\$ (3,260)	\$ (3,145)	\$ (3,026)	\$ (3,533)
Adjustments:					
Stock-based Compensation	(318)	(347)	(395)	(381)	(432)
FA impairment and/or write-off	-	-	(10)	(2)	(5)
GAAP Net Loss	\$ (3,565)	\$ (3,607)	\$ (3,550)	\$ (3,409)	\$ (3,970)

QuickLogic Leadership

Executives

Brian Faith

Chief Executive Officer, Director

- Joined in 1996
- CEO in 2016
- VP of Worldwide Marketing/ VP of Worldwide Sales & Marketing 2008-2016
- Rising managerial & executive positions, including engineering, product line management, marketing and sales

Sue Cheung, Ph.D.

Chief Financial Officer

- Joined in 2007
- CFO in 2017, VP of Finance in 2016
- Principal Accounting Officer, Corp. Controller & Asst. Controller 2007-2016
- Sr. Accounting management positions at Dell SonicWALL, VeriFone and other publicly traded and privately held companies
- CPA and PH.D. in Business Administration

Timothy Saxe, Ph.D.

Chief Technology Officer & SVP Engineering

- Joined in 2001
- CTO in 2008, SVP of Eng. in 2016
- Rising executive positions including VP of Eng. and VP of Software Eng.
- VP of FLASH Engineering at Actel Corp. and founder/CEO of semiconductor mfg. division at GateField Corp. (Zycad)
- Doctorate in Electrical Engineering

Rajiv Jain

VP Worldwide Operations

- Joined in 1992
- VP Worldwide Ops in 2014
- Sr. Dir. of Operations & Development Eng., Sr. Dir. of System Solutions & Process Technology, Dir. of Process Technology, and Sr. Process Technologist 1992-2014
- Sr. Engineering positions at National Semiconductor and Monolithic Memories

Board of Directors

E. Thomas Hart

Chairman

- QuickLogic CEO 1994-2009
- VP & GM of Advanced Networking Division at National Semiconductor Corp.
- Senior Manager, Motorola, Inc.

Michael R. Farese

Director

- 35 years in executive roles in telecomm & semiconductor industry including Antenna29; Entropic Comm. Inc.; BitWave Semicon, Palm; WJ Comm; Tropian Inc.; Motorola Corp.; Ericsson Inc.; Nokia Corp.; ITT Corp.; AT&T Corp.; and Bell Labs

Arturo Krueger

Director

- Over 40 years leadership in systems architecture, semiconductor design & development, operations and marketing
- Executive roles at Motorola, Inc. semiconductor products; Director, Marvell Technology Group Ltd

Andrew J. Pease

Director

- QuickLogic CEO/President 2009-2016, VP Worldwide Sales 2006-2009
- Executive roles at Broadcom Corp.; Synticity, Inc.
- Various sales positions at Advanced Micro Devices; Integrated Systems Inc.; and Vantis Corp.

Daniel A. Rabinovitsj

Director

- COO, Ruckus Wireless, Inc.
- Executive roles at Qualcomm Atheros, Inc.; Atheros Communications; NXP Semiconductors; ST Ericsson; and Silicon Labs

Christine Russell

Director

- CFO, UniPixel, Inc., Vendavo, Inc.,
- CFO positions at Evans Analytical Group; Virage Logic Corp.; OuterBay; Ceva, Inc.; and Persistence Software, Inc.

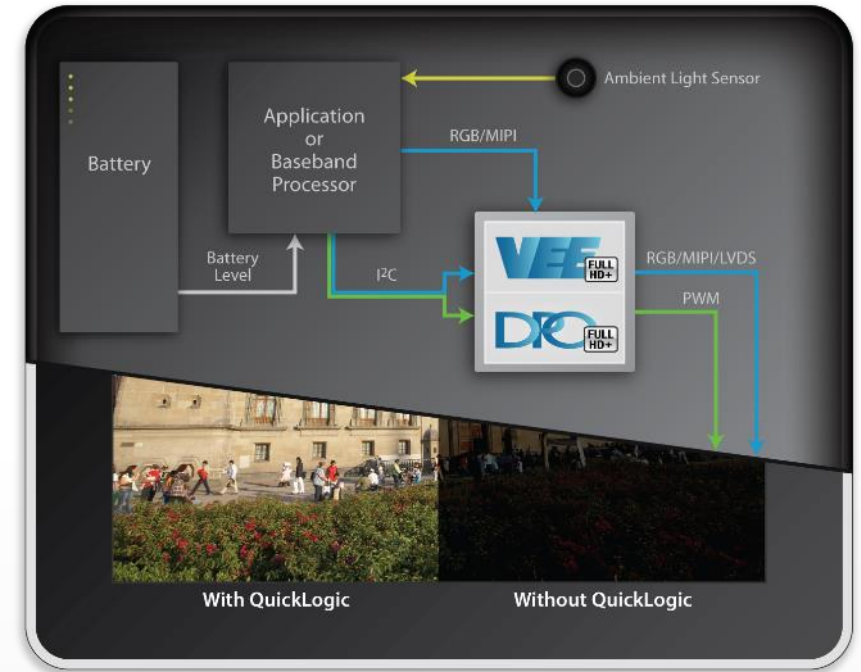
Gary H. Tauss

Director

- Exec. Director & CEO, BizTech
- Executive roles at Mobidia Technology, Inc.; InfiniRoute Networks, Inc.; LongBoard, Inc.; and TollBridge Technologies

Display Bridge & Connectivity Products – Maintaining Momentum

- Continue to ship into consumer tablet market
- Working with new IoT module using a QuickLogic connectivity solution – expect production 1H 2018
- Expect to continue display bridge revenue well into 2019 and beyond with large OEMs



CES 2018

Expanding the Market for Hearables/Wearables





Cleer

Stage Portable Bluetooth Speaker

Approved by Amazon for
Alexa Voice Services (AVS)



Cleer

EDGE Voice Headphone

Amazon's Alexa Voice Services
(AVS) via Bluetooth