

# **AnalogAI Selects memBrain™ SAGE Intellectual Property from Silicon Storage Technology® for its First Real-world Edge AI Processors**

## **Proprietary on-device training built to dynamically adapt to real-world environments**

CHANDLER, Ariz., Sept. 15, 2026 (GLOBE NEWSWIRE) -- AnalogAI has chosen memBrain™ Synaptic Analog Generative Engine (SAGE) neuromorphic hardware intellectual property (IP) from Microchip Technology's (**Nasdaq: MCHP**) Silicon Storage Technology® (SST®) subsidiary for its real-world edge AI processors. AnalogAI edge AI processors feature a hardware aware proprietary algorithm designed to simultaneously train and run inference on AI models in real-world environments.

AnalogAI is utilizing SST's memBrain SAGE IP to deliver high levels of analog compute-in-memory (aCIM) performance at or below one watt for ultra-low power edge applications. By leveraging the robust silicon proven SST memBrain SAGE IP based on SST SuperFlash®, AnalogAI is developing new capabilities for real-world inference applications such as environment adapting humanoid robots, drones and vehicles.

"AnalogAI is pursuing innovative co-optimized solutions for edge AI inference that enable edge devices to adapt real-time to sudden changes in the environment," said Mark Reiten, senior vice president of Microchip's Intelligent Compute business unit. "As the core inference engine for AnalogAI's first products, SST memBrain SAGE IP delivers the necessary compute performance coupled with the power efficiency AnalogAI requires to meet their application targets. We are pleased to welcome AnalogAI as the newest licensee of our SST memBrain IP, joining our rapidly expanding ecosystem."

SST memBrain SAGE IP features:

- memBrain Tensor In-Memory Logic Element (TILE)
  - memBrain solution optimized ESF3 bitcell which stores up to 8 bpc at nanoamp levels
  - memBrain solution custom array, decoders and driver circuitry
- Optimized DACs and ADCs
- Summator and high voltage bias circuitry
- Nanoamp level bitcell control logic
- memBrain proprietary test circuitry
- SST memBrain technology documentation and simulation models
- Full IP integration service and support

“AnalogAI selected SST’s memBrain SAGE IP after an industry-wide search of available offerings and determined the silicon-proven memBrain SAGE IP best enabled us to accelerate our development time while still achieving the ultra-low-power and high performance required in our market segment,” said Jaejun Lee, AnalogAI’s chief executive officer. “Analog Compute-in-Memory is a rapidly emerging field, and at AnalogAI we are pioneering new functionality for edge AI devices requiring real-world adaptation.”

SST’s memBrain SAGE IP has been developed and deployed in 40 nm and 28 nm foundry processes using production-ready SuperFlash memory. The current technology roadmap includes 22 nm memBrain SAGE IP development. Designed to provide reliable, high-performance and low-power non-volatile storage directly on the chip, SuperFlash memory is widely used in applications that require fast access times, high endurance and data retention without the need for external memory components.

### **Pricing and Availability**

Customers interested in SST’s memBrain IP solutions and SuperFlash technology should access the [SST website](#) or contact a [regional SST sales executive](#) for details. Those interested in AnalogAI’s products should visit the [AnalogAI website](#) or contact the AnalogAI team at [contact@analog-ai.com](mailto:contact@analog-ai.com).

### **Resources**

High-res images available through Flickr or editorial contact (feel free to publish):

- Application image:  
[www.flickr.com/photos/microchiptechnology/55472818028/sizes/o/](http://www.flickr.com/photos/microchiptechnology/55472818028/sizes/o/)

### **About Microchip Technology:**

Microchip Technology Inc. is a broadline supplier of semiconductors committed to making innovative design easier through total system solutions that address critical challenges at the intersection of emerging technologies and durable end markets. Its easy-to-use development tools and comprehensive product portfolio supports customers throughout the design process, from concept to completion. Headquartered in Chandler, Arizona, Microchip offers outstanding technical support and delivers solutions across the industrial, automotive, consumer, aerospace and defense, communications and computing markets. For more information, visit the Microchip website at [www.microchip.com](http://www.microchip.com).

### **About Silicon Storage Technology (SST):**

Microchip Technology’s SST subsidiary is a leading provider of embedded flash technology. SST develops, designs, licenses and markets a diversified range of proprietary and patented SuperFlash memory technology solutions for the consumer, industrial, automotive and Internet of Things (IoT) markets. SST was founded in 1989, went public in 1995 and was acquired by Microchip in April 2010. SST is now a wholly owned subsidiary of Microchip and is headquartered in San Jose, Calif. For more information, visit the SST website at [www.sst.com](http://www.sst.com).

### **About AnalogAI:**

AnalogAI builds edge AI processors that train directly on the device, allowing robots, drones and vehicles to adapt to changing physical environments in real time while running at ultra-low power. Its analog compute-in-memory architecture performs on-device training and inference in a single low-power design, moving beyond the inference-only approach of conventional edge AI hardware. AnalogAI targets physical-world edge applications where systems must keep adapting after deployment, from robotics to autonomous machines. The

company is headquartered in the Seoul area, South Korea. For more information, visit [www.analog-ai.com](http://www.analog-ai.com).

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**Editorial Contact:**

Brian Thorsen

480-792-7182

[Brian.Thorsen@microchip.com](mailto:Brian.Thorsen@microchip.com)

**Partner Media Contact:**

Jaejun Lee

[jaejun.lee@analog-ai.com](mailto:jaejun.lee@analog-ai.com)

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