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AMD Unveils Ambidextrous Computing Roadmap

Announces 64-bit ARM Core Architecture License and Future "K12" ARM-based Core; Pin-Compatible x86 and ARM Processors; Demonstrates AMD Opteron A-Series 64-bit ARM Server CPU Codename "Seattle"

SAN FRANCISCO, CA -- (Marketwired) -- 05/05/14 -- AMD (NYSE: AMD) today announced a roadmap of near- and mid-term computing solutions that harness the best characteristics of both the x86 and ARM® ecosystems, called "ambidextrous computing." The cornerstone of this roadmap is the announcement of AMD's 64-bit ARM architecture license for the development of custom high-performance cores for high-growth markets. Today's announcement also provides a forward-looking glimpse into AMD's development plans to deliver truly unmatched ambidextrous computing and graphics performance using a shared, flexible infrastructure to enable its customers to blaze new paths of innovation for the embedded, server and client markets as well as semi-custom solutions.

"Before today, AMD was the only company in the world to deliver high performance and low-power x86 with leadership graphics. AMD now takes a bold step forward and has become the only company that can provide high-performance 64-bit ARM and x86 CPU cores paired with world-class graphics," said Rory Read, AMD president and CEO. "Our innovative ambidextrous design capability, combined with our portfolio of IP and expertise with high-performance SoCs, means that AMD is set to deliver ambidextrous solutions that enable our customers to change the world in more efficient and powerful ways."

The market for ARM- and x86-based processors is expected to grow to more than $85 billion by 2017(1). AMD is uniquely positioned as the only company delivering differentiated solutions capable of addressing the breadth of this market. This is the first time a major processor provider has created the IP path to allow others to leverage innovation across both ARM and x86 ecosystems.

AMD's ambidextrous computing roadmap includes:

- "Project SkyBridge" - This design framework, available starting in 2015, will feature a new family of 20 nanometer APUs and SoCs that are expected to be the world's first pin-compatible ARM and x86 processors. The 64-bit ARM variant of "Project SkyBridge" will be based on the ARM Cortex®-A57 core and is AMD's first Heterogeneous System Architecture ("HSA") platform for Android; the x86 variant will feature next-generation "Puma+" CPU cores. The "Project SkyBridge" family will feature full SoC integration, AMD Graphics Core Next technology, HSA, and AMD Secure Technology via a dedicated Platform Security Processor (PSP).
- "K12" - A new high-performance, low-power ARM-based core that takes deep advantage of AMD's ARM architectural license, extensive 64-bit design expertise, and a core development team led by Chief CPU Architect Jim Keller. The first products based on "K12" are planned for introduction in 2016.

"At ARM we are dedicated to working with partners who revolutionize and transform experiences everywhere from sensors to servers," said Simon Segars, CEO at ARM. "AMD's market reach and proven experience in leading industry transitions to 64-bit computing in client and server environments, combined with ARM's low power expertise and server base system architecture (SBSA) standard, will deliver new capabilities and drive innovation across multiple high growth markets."

AMD today also publicly demonstrated for the first time its 64-bit ARM-based AMD Opteron™ A-Series processor, codenamed "Seattle," running a Linux environment derived from the Fedora Project. The Fedora Project is a Red Hat-sponsored, community-driven Linux distribution, providing a familiar, enterprise class operating environment to developers and IT administrators worldwide. This Fedora Project-based Linux environment enables companies to transition to ARM-based servers without the need to integrate entirely new tools and software platforms to their IT environments. This demonstration represents a significant step forward in expanding the footprint of ultra-efficient 64-bit ARM processers within the data center.

Today's news was disclosed at a press conference and webcast held at the Ritz-Carlton in San Francisco. A replay of the webcast will made available at approximately noon Eastern tomorrow at ir.amd.com.

Supporting Resources

- Learn more about AMD's Opteron A-Series processor
- The core roadmap and presentation can be viewed at ir.amd.com
- Slides, images and additional materials can be downloaded at http://bit.ly/1kuhOxh
- Become a fan of AMD on Facebook
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About AMD
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(1) Based on AMD's internal estimates.

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Cautionary Statement

This Press Release contains forward-looking statements concerning AMD, including: the features and functionality of new AMD products and the timing and availability of such products; our ability to offer both X86 and ARM processors for embedded, server, client and semi-custom markets; our ability to create high performance cores for our embedded, server and low-power client markets; our ability to deliver and execute on our X86 and 64-bit ARM Ambidextrous Computing Roadmap; which are made pursuant to the safe harbor provisions of the Private Securities Litigation Reform Act of 1995. Forward-looking statements are commonly identified by words such as "would," "may," "expects," "believes," "plans," "intends," "projects," and other terms with similar meaning. Investors are cautioned that the

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