

# Scale Space Applications with COTS-to-Radiation-Tolerant and Radiation-Hardened Arm® Core MCUs

Devices enable designers to begin development with a commercial device before moving to different levels of radiation-qualified versions, reducing development time and costs

CHANDLER, Ariz., March 28, 2019 (GLOBE NEWSWIRE) -- From NewSpace to critical space missions, designers of space applications need to reduce design cycles and costs while scaling development across missions with different radiation requirements. To support this trend, Microchip Technology Inc. (Nasdaq: MCHP) today introduced the space industry's first Arm<sup>®</sup>-based microcontrollers (MCUs) that combine the low-cost and large ecosystem benefits of Commercial Off-the-Shelf (COTS) technology with space-qualified versions that have scalable levels of radiation performance. Based on the automotive-qualified SAMV71, the SAMV71Q21RT radiation-tolerant and SAMRH71 radiation-hardened MCUs implement the widely deployed Arm<sup>®</sup> Cortex<sup>®</sup>-M7 System on Chip (SoC), enabling more integration, cost reduction and higher performance in space systems.

The SAMV71Q21RT and SAMRH71 allow software developers to begin implementation with the SAMV71 COTS device before moving to a space-grade component, significantly reducing development time and cost. Both devices can use the SAMV71's full software development toolchain, as they share the same ecosystem including software libraries, Board Support Package (BSP) and Operating System (OS) first level of porting. Once preliminary developments are complete on the COTS device, all software development can be easily swapped out to a radiation-tolerant or radiation hardened version in a high-reliability plastic package or space-grade ceramic package. The SAMV71Q21RT radiation-tolerant MCU reuses the full COTS mask set and offers pinout compatibility, making the transition from COTS to qualified space parts immediate.

While the SAMV71Q21RT's radiation performance is ideal for NewSpace applications such as Low Earth Orbit (LEO) satellite constellations and robotics, the SAMRH71 offers the radiation performance suited for more critical sub-systems like gyroscopes and star tracker equipment. The SAMV71Q21RT rad-tolerant device ensures an accumulated TID of 30Krad (Si) with latch up immunity and is nondestructive against heavy ions. Both devices are fully immune to Single-Event Latchup (SEL) up to 62 MeV.cm²/mg.

The SAMRH71 radiation-hardened MCU is designed specifically for deep space applications with the following targeted radiation performances:

- Accumulated TID of more than 100 Krad (Si)
- No Single Event Upset (SEU) Linear Energy Transfer (LET) up to 20 MeV.cm²/mg, without system mitigation

 Designed for No Single-Event Functional Interrupts (SEFI), which secures all memories' integrity

Based on the Arm Cortex-M7 core, the SAMV71Q21RT and SAMRH71 feature high-performance and low-power operation to provide long operating life for aerospace applications. To protect against the effects of radiation and manage system mitigation, the architecture of the devices includes fault management and data integrity features such as Error-correcting Code (ECC) memory, Integrity Check Monitor (ICM) and Memory Protection Unit (MPU). The SAMV71Q21RT and SAMRH71 also feature CAN FD and Ethernet AVB/TSN capabilities to follow the evolution of space systems' connectivity requirements. To further support deep space applications, the SAMRH71 has dedicated SpaceWire and MIL-STD-1553 interfaces for control and high-speed data management up to 200Mbit/s.

"As the industry's first radiation-tolerant and radiation-hardened Arm Cortex-M7 MCUs, the SAMV71Q21RT and SAMRH71 bring a proven SoC architecture from the automotive market to aerospace applications," said Bob Vampola, vice president of Microchip's aerospace and defense business unit. "Leveraging Microchip's COTS-to-radiation-tolerant and radiation-hardened approach, the devices allow designers to begin prototyping immediately at a relatively limited cost before moving forward with qualified parts."

## **Development Tools**

To ease the design process and accelerate time to market, developers can use the ATSAMV71-XULT evaluation board. The devices are supported by Atmel Studio Integrated Development Environment (IDE) for developing, debugging and software libraries. Both devices will also be supported in MPLAB® Harmony version 3.0 by mid-2019.

# **Pricing and Availability**

The SAMRH71 in CQFP256 ceramic package is sampling today, and the SAMV71Q21RT is available today in volume production quantities in four derivatives:

- SAMV71Q21RT-DHB-E in ceramic prototype QFP144 package
- SAMV71Q21RT-DHB-MQ in ceramic space-grade QFP144 package, QMLQ equivalent
- SAMV71Q21RT-DHB-SV in ceramic space-grade QFP144 package, QMLV equivalent
- SAMV71Q21RT-DHB-MQ in plastic QFP144 package, AQEC high reliability qualified

For additional information and to purchase products mentioned here, contact a Microchip sales representative or authorized worldwide distributor.

#### Resources

High-res images available through Flickr or editorial contact (feel free to publish):

- Application image: <a href="https://www.flickr.com/photos/microchiptechnology/46749696124">https://www.flickr.com/photos/microchiptechnology/46749696124</a>
- SAMV71Q21RT chip shot: <a href="https://www.flickr.com/photos/microchiptechnology/46749696434/in/photostream/">https://www.flickr.com/photos/microchiptechnology/46749696434/in/photostream/</a>
- SAMRH71 chip shot: <a href="https://www.flickr.com/photos/microchiptechnology/46749696894">https://www.flickr.com/photos/microchiptechnology/46749696894</a>
- Diagram: <a href="https://www.flickr.com/photos/microchiptechnology/46565369375">https://www.flickr.com/photos/microchiptechnology/46565369375</a>

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