

SensiML and Neurosense Announce Strategic Partnership for Highly Integrated Intelligent Edge Technology Platform

- *SensiML Analytics Toolkit makes AI for IoT endpoint applications easy and fast to implement*
- *Neurosense manufactures a highly integrated low-power IoT sensor SoC optimized for AI*
- *Partnership delivers turnkey solution and development services for consumer IoT device OEMs*



Portland, OR – July 10, 2019 – SensiML Corporation, a leading developer of AI tools for building intelligent IoT endpoints, and Neurosense, a provider of highly integrated intelligent IoT SoCs, today announced a strategic partnership to provide a combined solution for developers seeking to build intelligent small form factor IoT devices.

The [SensiML AI Analytics Toolkit](#) brings real-time artificial intelligence to the sensor endpoint with a platform that is easily accessible to any application developer. SensiML's analytic engine takes data sets and creates an optimized device-ready AI algorithm that balances the desired accuracy with the resource constraints of the target hardware. This algorithm is automatically compiled to optimize machine code that can run in real time on the target embedded platform, making it easy for developers to implement AI at the endpoint quickly and efficiently.

[Neurosense](#) specializes in edge computing technology, providing an integrated System-In-Package (SIP) platform with an embedded customized pattern matching algorithm for motion detection and sound recognition. The Neurosense ELA106 platform combines processing, communications, and sensing in one package. The two companies will combine the capabilities of SensiML's Toolkit with the Neurosense platform to give IoT developers a complete motion detection and sound recognition solution for endpoint applications.

"The SensiML Data Analytics Toolkit makes it extremely easy for manufacturers to implement intelligent sensing directly on the endpoint device without the need for large teams of data scientists and firmware engineers," said KS Kim, chief executive officer of Neurosense. "Their toolkit complements our edge computing SIP platform and enables us to deliver an end-to-end solution to our mutual customer base."

“Neurosense has proven to be a great partner to showcase the possibilities for endpoint intelligence using SensiML as the analytics engine for their ELA106 system solution with its highly integrated combination of processor, communications, and physical sensing all in one system package.” said Chris Rogers, CEO of SensiML.

Availability

The SensiML AI Analytics Toolkit is available today. The Neurosense ELA106 intelligent endpoint SIP will be available to customers starting mid-August 2019.

About SensiML

SensiML, a subsidiary of QuickLogic, offers cutting-edge software that enables ultra-low power IoT endpoints that implement AI to transform raw sensor data into meaningful insight at the device itself. The company’s flagship solution, the SensiML Analytics Toolkit, provides an end-to-end development platform spanning data collection, labeling, algorithm and firmware auto generation, and testing. The SensiML Toolkit supports Arm® Cortex®-M class and higher microcontroller cores, Intel® x86 instruction set processors, and heterogeneous core QuickLogic SoCs and QuickAI platforms with FPGA optimizations. For more information, visit www.sensiml.com.

About Neurosense

Neurosense was established as a startup company fully dedicated to developing and providing customized edge intelligence algorithms for motion detection and sound recognition together with System-in-Package (SIP) platforms designed to easily implement the Neurosense AI algorithm. It features high accuracy and runs on a low computing power Arm® core-based CPU with unique software-based edge intelligence algorithms developed by the Neurosense design team. Applications include IoT sensors for industrial uses such as predictive maintenance for motors, bearings and conveyor belts, wearable devices, toys, light sticks for pop concerts, voice control of consumer electronics, smart building control, etc. For more information, please visit www.neurosense.co.kr

SensiML and logo are trademarks of SensiML. All other trademarks are the property of their respective holders and should be treated as such.

Press Contacts:

SensiML
Andrea Vedanayagam
Veda Communications
408.656.4494
pr@quicklogic.com

Neurosense
Steve Lee
Administration officer
82-10-4830-2620
swlee@neurosense.co.kr