

# **QuickLogic to Present at Sensors Expo** and Conference 2019

SUNNYVALE, Calif., June 25, 2019 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), a developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, and Endpoint AI solutions, today announced that it will be presenting and demonstrating at the Sensors Expo and Conference at the San Jose Convention Center.



#### Presentation

Who: Sam Massih, Director Product Management

When: Wednesday, June 26 at 3:30 p.m.

What: Al at the Endpoint Device

This presentation will outline how implementing intelligent IoT devices requiressensor data collection, analysis, and AI to determine actionable steps. Most applications today send sensor data to the cloud for processing and abstraction. However, this approach stresses the system's bandwidth and power supply, adds latency and reduces security. Implementing AI at the endpoint devices solves these system challenges. A complete AI Endpoint solution should consider data analytics, model building, and hardware implementation to deliver a low power, scalable solution. This session teaches attendees how to build intelligent endpoints quickly and easily from start to finish, optimized for low power at multiple levels and with significant time to market gains.

#### **Booth Demonstration**

When: Wednesday, June 26, 10am - 6pm and Thursday, June 27, 10am - 4pm

Where: Booth number 526

**What:** QuickLogic will be demonstrating the use of its endpoint AI toolkit for developing industrial predictive maintenance smart sensors. The demo will show the end-to-end

implementation workflow including the critical data collection and labeling phase through endpoint model testing on the device.

## **About QuickLogic**

QuickLogic develops low power, multi-core semiconductor platforms and Intellectual Property (IP) for Artificial Intelligence (AI), voice and sensor processing. The solutions include an embedded FPGA IP (eFPGA) for hardware acceleration and pre-processing, and heterogeneous multi-core SoCs that integrate eFPGA with other processors and peripherals. The Analytics Toolkit from the company's wholly-owned subsidiary, SensiML completes the 'full stack' end-to-end solution with accurate sensor algorithms using AI technology. The full range of platforms, software tools and eFPGA IP enables the practical and efficient adoption of AI, voice and sensor processing across the multitude of mobile, wearable, hearable, consumer, industrial, edge and endpoint IoT applications. For more information, visit www.quicklogic.com and https://www.quicklogic.com/blog/.

### About SensiML

SensiML, a subsidiary of QuickLogic, offers cutting-edge software that enables ultra-low power IoT endpoints that implement AI to transform raw sensor data into meaningful insight at the device itself. The company's flagship solution, the SensiML Analytics Toolkit, provides an end-to-end development platform spanning data collection, labeling, algorithm and firmware auto generation, and testing. The SensiML Toolkit supports Arm® Cortex®-M class and higher microcontroller cores, Intel® x86 instruction set processors, and heterogeneous core QuickLogic SoCs and QuickAI platforms with FPGA optimizations. For more information, visit www.sensiml.com.

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