

QuickLogic to Exhibit at DAC 2019

Sunnyvale, CA – May 30, 2019 – QuickLogic Corporation (NASDAQ: QUIK), a developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, and Endpoint AI solutions, today announced that it will be exhibiting at the Design Automation Conference (DAC) in Las Vegas, NV.

QuickLogic will be demonstrating the benefits of AI with a predictive maintenance demo. This demo will showcase how easily and quickly endpoint AI and machine learning applications can be implemented without needing an army of data scientists or AI experts. This solution is easily deployable for industrial use cases which perform predictive maintenance or visual inspection using endpoint IoT devices. QuickLogic will also be showing the SensiML Analytics Toolkit, a complete end-to-end AI software platform, that transforms raw sensor data into business process insight without the need for machine learning expertise.

When: Monday, June 3 - Wednesday, June 5, 10am - 6pm

Where: Booth 962

Location: Las Vegas Convention Center

For more information, conference program schedules and exhibit hours, please visit https://dac.com

About QuickLogic

QuickLogic Corporation (NASDAQ: QUIK) is a fabless semiconductor company that develops low power, multi-core semiconductor platforms and Intellectual Property (IP) for Artificial Intelligence (AI), voice and sensor processing. The solutions include embedded FPGA IP (eFPGA) for hardware acceleration and pre-processing, and heterogeneous multi-core SoCs that integrate eFPGA with other processors and peripherals. The Analytics Toolkit from our recently acquired wholly-owned subsidiary, SensiML Corporation, completes the end-to-end solution with accurate sensor algorithms using AI technology. The full range of platforms, software tools and eFPGA IP enables the practical and efficient adoption of AI, voice and sensor processing across mobile, wearable, hearable, consumer, industrial, edge and endpoint IoT. For more information, visit www.quicklogic.com/blog/.

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