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Vietnam Becomes Third-largest Marvell Global R&D Hub as Engineering Workforce Surpasses 500 Employees

SANTA CLARA, Calif., September 30, 2025 – [Marvell Technology, Inc.](https://www.marvell.com) (NASDAQ: MRVL), a leader in data infrastructure semiconductor solutions, today announced the opening of three new offices in Vietnam. The new locations—eTown6 and UOA Tower in Ho Chi Minh City and ICT1 in Da Nang—support the rapidly growing Marvell engineering workforce and reinforce the company’s commitment to innovation in AI data center integrated circuit (IC) design.

The new facilities in Ho Chi Minh City feature a state-of-the-art R&D lab equipped with advanced validation tools for testing semiconductor chips.

“The new lab reflects our deep and enduring commitment to technical excellence and our strategic confidence in Vietnam’s role as a rising hub for semiconductor innovation,” said Sandeep Bharathi, president of the Data Center Group at Marvell. “We are very excited to build an extremely capable engineering team in Vietnam. They will be driving breakthroughs across some of our most complex and high-performance products that are shaping the future of AI data infrastructure.”

Commitment to Local Talent: From 5 to over 500 Engineers

Marvell Vietnam was established in 2013 with five engineers at the Tan Thuan EPZ facility in Ho Chi Minh City. Initially focused on design verification, the team gradually expanded its scope to cover the full spectrum of IC design, including architecture, digital design, analog mixed-signal design, analog layout, emulation, design-for-test, synthesis, static timing analysis, physical design, software and firmware development, CAD tools, and hardware design and validation.

In 2023, Marvell announced plans to expand its Vietnam workforce by 50 percent over three years. Today, with more than 500 engineers, Vietnam has become the third-largest Marvell R&D center after the United States and India.

Vietnam is a strategic region for the company’s growth strategy, including a focus on fostering greater diversity and increasing the number of female engineers. To support these goals, Marvell continues to collaborate with Vietnamese universities and the government through a combination of scholarship programs, internships, curriculum co-development and technical seminars to cultivate the next generation of semiconductor talent. The company awards 30 scholarships to outstanding students each year and has hired an average of around 50 interns per year, offering hands-on experience in IC design and exposure to real-world innovation.

“Vietnam has established itself as a strategic pillar in the Marvell global R&D network,” said Quang Dam Le, general manager of Marvell Vietnam. “Our engineers are driving some of the industry’s most advanced technologies, and the rapid growth of our team reflects the strength of the local talent. These new offices mark an exciting new chapter for Marvell and for Vietnam’s emergence as a center of excellence in semiconductor design.”

Next-generation Semiconductor Innovation

Engineers at Marvell Vietnam are at the forefront of some of the industry’s most advanced and demanding semiconductor initiatives, harnessing their extensive expertise in IC design and the latest semiconductor process technologies. Their contributions include the development of the [Marvell® Alaska® A 800G PAM4 DSP Active Electrical Cable \(AEC\)](#), [Alaska P PCIe Gen6 Retimer](#) and [Nova 1.6 Tbps PAM4 DSP](#), among others.

Opening ceremonies for the new offices will take place on September 30, 2025, in Ho Chi Minh City, and on October 1, 2025, in Da Nang.

About Marvell

To deliver the data infrastructure technology that connects the world, we’re building solutions on the most powerful foundation: our partnerships with our customers. Trusted by the world’s leading technology companies for over 30 years, we move, store, process and secure the world’s data with semiconductor solutions designed for our customers’ current needs and future ambitions. Through a process of deep collaboration and transparency, we’re ultimately changing the way tomorrow’s enterprise, cloud and carrier architectures transform—for the better.

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