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Intel Foundry Services Launches Ecosystem Alliance to Accelerate Customer Innovation

IFS Accelerator brings together leading design ecosystem partners to deliver a full spectrum of solutions and services for customers' design needs.

SANTA CLARA, Calif.--(BUSINESS WIRE)-- **What's New:** Intel Foundry Services (IFS) today launched Accelerator, a comprehensive ecosystem alliance designed to help foundry customers smoothly bring their silicon products from idea to implementation. Through deep collaboration with a group of industry-leading companies across electronic design automation (EDA), intellectual property (IP) and design services, IFS Accelerator taps the best capabilities available in the industry to help advance customer innovation on Intel's foundry manufacturing platform.

This press release features multimedia. View the full release here: <https://www.businesswire.com/news/home/20220207005214/en/>



A photo from November 2021 shows employees in clean room "bunny suits" working at Intel's D1X factory in Hillsboro, Oregon. A D1X fab expansion due for completion in 2022 will help meet a sharply increasing global demand for semiconductors. (Credit: Intel Corporation)

"A vibrant semiconductor design ecosystem is critical to the success of our foundry. We are pleased to launch our ecosystem alliance program with leading design companies that will play a vital role in accelerating our foundry customers' success."

—Pat Gelsinger, Intel CEO

How It Works: IFS launched Accelerator's preliminary phase in September 2021 to help automotive chip

designers transition to more advanced process technologies by offering both custom and industry-standard intellectual property. With today's comprehensive launch of the IFS

Accelerator – including 17 founding partner companies – the ecosystem alliance is enhanced with full support from electronic design automation and design services providers, as well as a broad library of IP offerings from a range of partners.

The IFS Accelerator provides a comprehensive suite of tools for customers:

- The most powerful and validated EDA solutions optimized for Intel’s leadership technology and manufacturing, covering the full spectrum from concept to high-volume silicon production.
- A comprehensive, silicon-verified, and Intel process-specific IP portfolio, including standard cell libraries, embedded memories, general purpose I/Os, analog IP and interface IP.
- Design services partners that allow customers to focus on creating unique product ideas, assigning implementation tasks to rigorously trained designers well-versed on Intel technology.

Why It’s Important: These three capabilities are the foundation of customer interaction with a foundry manufacturing partner. EDA suppliers produce tools that assist in the specification, planning, design, verification, implementation and testing of electronic systems. Collaborating closely with EDA partners allows customers to co-optimize and enhance tools and flows so chip designers can best realize their performance, power and area (PPA) goals, while accelerating their time to market.

“Foundry customers need access to design services, IP, and tools and flows to enable their next-generation products in different stages,” said Randhir Thakur, president of Intel Foundry Services. “With the goal of accelerating customer innovation, the IFS Accelerator ecosystem alliance program brings together the brightest minds and the broadest capabilities to deliver a seamless interface with Intel’s process and packaging technologies. We are entering a new era of openness in technology, and Intel is wholeheartedly embracing the idea that innovation thrives in an open and collaborative environment.”

Designing products with integrated and reusable circuit IP blocks has become a significant trend as system-on-chip (SoC) design has grown increasingly complex. IP partners collaborate with IFS to enable designers to access high-quality IP that aligns with their aggressive design and project schedule requirements. The IFS Accelerator IP portfolio includes essential IP blocks needed for modern SoCs – all optimized for IFS technologies.

Designing the next generation of semiconductor products requires skilled engineering talent and resources, especially when working with leading-edge process technologies. Partnering with IFS Accelerator design service providers gives customers options for additional support when bringing their ideas to life, with specialties ranging from analog and digital physical designs to low-level system software. Depending on a customer’s needs, the silicon experts from the alliance partners can assist in different stages, including design, verification, implementation, and emulation.

Leveraging the capabilities of this ecosystem is more important than ever as the demand for semiconductors grows across markets and applications. IFS is making its technology platforms available to these partners to ensure the pace of innovation moves forward at a torrid pace. Read more about Intel’s renewed commitment to openness in Thakur’s editorial, [“Innovation Starts Here: Partnering to Power the Foundry Ecosystem.”](#)

Who's Involved: The IFS Accelerator features innovative partner companies across each of the three pillars of the program:

- **EDA Alliance:** Ansys, Cadence, Siemens EDA, Synopsys
- **IP Alliance:** Alphawave, Analog Bits, Andes, Arm, Cadence, eMemory, M31, SiFive, Silicon Creations, Synopsys, Vidatronic
- **Design Services Alliance:** Capgemini, Tech Mahindra, Wipro

For more details and supporting quotes from these partners, please see "[Building an Open Ecosystem](#)."

More Context: [Building an Open Ecosystem](#) (Quote Sheet) | [Intel Foundry Services Accelerator](#) | [Innovation Starts Here: Partnering to Power the Foundry Ecosystem](#)(Randhir Thakur Editorial) | [Intel Foundry Services](#) | [Intel Launches \\$1 Billion Fund to Build a Foundry Innovation Ecosystem](#)

About Intel

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