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AMD and Synopsys Expand IP Partnership

SUNNYVALE, CA and MOUNTAIN VIEW, CA -- (Marketwired) -- 09/18/14 -- ***AMD*** (NYSE: AMD)

Highlights:

- Multi-year agreement gives AMD access to a range of Synopsys design IP including interface, memory compiler, logic library and analog IP for advanced FinFET process nodes
- Synopsys acquires rights to AMD's interface and foundation IP, and hires a team of engineers from AMD with IP R&D expertise
- These agreements enable AMD to realize ongoing engineering efficiencies and focus engineering efforts on product differentiation

Synopsys, Inc. (NASDAQ: SNPS), a global leader providing software, IP and services used to accelerate innovation in chips and electronic systems and AMD (NYSE: AMD) today announced they have signed a multi-year agreement that gives AMD access to a range of Synopsys DesignWare® interface, memory compiler, logic library and analog IP on advanced 16/14-nanometer (nm) and 10-nm FinFET process technologies. Synopsys is also hiring approximately 150 AMD IP R&D engineers and gains access to AMD's leading interface and foundation IP.

Synopsys is a demonstrated leader in developing silicon-proven IP for advanced process technologies, providing designers with a broad range of high-quality IP for integration into System-on-Chips (SoCs) and delivering expert technical support. For 45 years, AMD has developed and integrated many forms of complex IP into advanced processors, graphics cards and related SoCs. By licensing proven, standard IP from Synopsys and transferring interface and foundation IP to Synopsys, AMD can focus its valuable engineering resources on its ongoing product differentiation and IP reuse strategy and realize long-term cost efficiencies.

"Today's announcement aligns with AMD's continuing IP development strategy to focus our internal teams on designing the innovative 64-bit processor, graphics and peripheral IP that forms the foundation for our competitive differentiation, while leveraging Synopsys, the industry leader for cost-effective development of complementary standard IP components, for our future SoCs," said Mark Papermaster, AMD senior vice president and CTO. "We've partnered with Synopsys for tools and IP for more than a decade, and this expanded relationship is a great example of leveraging high-quality, standard IP for cost-effective reuse across multiple solutions."

"We are excited to expand our relationship with AMD through this partnership and look forward to their experienced IP R&D team joining Synopsys," said Joachim Kunkel, senior vice president and general manager of the Solutions Group at Synopsys. "These agreements are an example of how our customers are engaging with us in long-term, collaborative partnerships to meet their IP requirements and enable them to focus their efforts on product differentiation."

AMD cost efficiencies from these agreements are contemplated in the company's 2014 quarterly non-GAAP operating expense guidance of approximately $420 million to $450 million.

About Synopsys
Synopsys, Inc. (NASDAQ: SNPS) accelerates innovation in the global electronics market. As a leader in electronic design automation (EDA) and semiconductor IP, Synopsys delivers software, IP and services to help engineers address their design, verification, system and manufacturing challenges. Since 1986, engineers around the world have been using Synopsys technology to design and create billions of chips and systems. Learn more at www.synopsys.com.

About AMD
AMD (NYSE: AMD) designs and integrates technology that powers millions of intelligent devices, including personal computers, tablets, game consoles and cloud servers that define the new era of surround computing. AMD solutions enable people everywhere to realize the full potential of their favorite devices and applications to push the boundaries of what is possible. For more information, visit www.amd.com.

Cautionary Statement
This press release contains forward-looking statements concerning AMD, including the anticipated benefits from AMD's multi-year agreement with Synopsys; AMD's IP development strategy; AMD's non-GAAP operating expense guidance for the third quarter of 2014, which are made pursuant to the safe harbor provisions of the Private Securities Litigation Reform Act of 1995. Forward-looking statements are commonly identified by words such as "believes, "expects," "may," "will," "should," "seeks," "intends," "pro forma," "estimates," "anticipates," "plans," "projects," "would" and other terms with similar meaning. Investors are cautioned that the forward-looking statements in this release are based on current beliefs, assumptions and expectations, speak only as of the date of this release and involve risks and uncertainties that could cause actual results to differ materially from current expectations. Risks include the possibility that Intel Corporation's pricing, marketing and rebating programs, product bundling, standard setting, new product introductions or other activities may negatively impact AMD's plans; that AMD will require additional funding and may be unable to raise sufficient capital on favorable terms, or at all; that customers stop buying AMD's products or materially reduce their operations or demand for AMD's products; that AMD may be unable to develop, launch and ramp new products and technologies in the volumes that are required by the market at mature yields on a timely basis; that AMD's third-party foundry suppliers will be unable to transition its products to advanced manufacturing process technologies in a timely and effective way or to manufacture its products on a timely basis in sufficient quantities and using competitive process technologies; that AMD will be unable to obtain sufficient manufacturing capacity or components to meet demand for its products or will not fully utilize our projected manufacturing capacity needs at GF's microprocessor manufacturing facilities; that AMD's requirements for wafers will be less than the fixed number of wafers that it agreed to purchase from GLOBALFOUNDRIES INC. (GF) or GF encounters problems that significantly reduce the number of functional die AMD receives from each wafer; that AMD is unable to successfully implement its long-term business strategy; that AMD inaccurately estimates the quantity or type of products that its customers will want in the future or will ultimately end up purchasing, resulting in excess or obsolete inventory; that AMD is unable to manage the risks related to the use of its third-party distributors and add-in-board (AIB) partners or offer the appropriate incentives to focus them on the sale of its products; that AMD may be unable to maintain the level of investment in research and development that is required to remain competitive; that there may be

unexpected variations in market growth and demand for AMD's products and technologies in light of the product mix that it may have available at any particular time; that global business and economic conditions will not improve or will worsen; that PC market conditions do not improve or will worsen; that demand for computers will be lower than currently expected; and the effect of political or economic instability, domestically or internationally, on AMD's sales or supply chain. Investors are urged to review in detail the risks and uncertainties in AMD's Securities and Exchange Commission filings, including but not limited to the Quarterly Report on Form 10-Q for the quarter ended June 28, 2014.

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