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AMD Fusion APU Codenamed "Llano" Demonstrated at 6th Annual AMD Technical Forum & Exhibition 2010

First Public Demonstration of 32nm "Llano" Unveils High-Performance AMD Fusion APU's Heterogeneous Computing Power for Taiwan Ecosystem Partners

TAIPEI, TAIWAN -- (MARKET WIRE) -- 10/19/10 -- At the 6th Annual AMD Technical Forum & Exhibition (TFE) 2010, AMD (NYSE: AMD) today showcased for its ecosystem partners the first public demonstration of the forthcoming AMD Fusion Accelerated Processing Unit (APU) codenamed "Llano," designed for notebook, ultrathin and desktop PCs. AMD demonstrated the accelerated single-chip processing muscle of Llano by simultaneously processing three separate compute-and graphics-intensive workloads.

"The serial and powerful parallel processing capability of the Llano APU has the potential to make OEMs and consumers re-think their computing experience," said Chris Cloran, corporate vice president and general manager, client division, AMD. "The experience potential of Llano is truly incredible, and the demos we showed today on stage provide a glimpse of what this processor is capable of delivering in sleek form factors with long battery life. Everything consumers love about their digital lifestyles today -- social networking, gaming, consuming and creating media -- can be enhanced with Llano, enabling a more interactive, vivid and immersive experience."

The Llano APU demo showed three compute-intensive workloads simultaneously on Microsoft Windows® 7, including calculating the value of Pi to 32 million decimal places, and decoding HD video from a Blu-ray disc. Running concurrent to the CPU and HD video playback applications, Microsoft's nBody DirectCompute application is shown achieving around 30 GFLOPS (as reported in the application) a relative measure of the available capacity to post-process video during playback, play a DirectX11 game, or assist the CPU cores to accelerate a non-graphics application. The demonstration represents a preview of Llano's raw compute power enabling new levels of experience computing that AMD aims to bring to mainstream PC users in 2011.

Held annually in Taiwan, the AMD Technical Forum & Exhibition is an ecosystem partner event that focuses on addressing the world's most complex technology challenges, and spotlighting technology breakthroughs. Exhibitors span academia, hardware and software industries, fostering a healthy, open ecosystem for the AMD Fusion family of APUs.

For more information please visit http://fusion.amd.com.

Additional Resources

- Video: Chris Cloran previews Llano at AMD Technical Forum and Exhibition

- Blog: AMD and its partner ecosystem showcases AMD Fusion at TFE
- AMD will be hosting the AMD Fusion Developer Summit June 13-16, 2011 in Seattle, WA. Visit the conference website to learn more.

Cautionary Statement
This release contains forward-looking statements concerning the timing of the launch and ramp of new products and technologies and the features of these products, which are made pursuant to the safe harbor provisions of the U.S. Private Securities Litigation Reform Act of 1995. Forward-looking statements are generally preceded by words such as "plans," "expects," "believes," "anticipates" or "intends." Investors are cautioned that all forward-looking statements in this release involve risks and uncertainty that could cause actual results to differ materially from current expectations, including the risk that the AMD products described herein will not be available in the mix required by the market and at mature yields on a timely basis; AMD's technology partners will not offer these AMD products as described herein, or in sufficient quantities to address market demand; Intel Corporation's pricing, marketing and rebating programs, product bundling, standard setting, new product introductions or other activities targeting AMD's business will prevent attainment of AMD's current plans; there will be unexpected variations in market growth and demand for these AMD products; and AMD will be unable to obtain sufficient manufacturing capacity or components to meet demand for these products. We urge investors to review in detail the risks and uncertainties in AMD's filings with the United States Securities Exchange Commission.

About AMD
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