

February 22, 2016



Silicon Storage Technology Announces Availability of its Smartbit® OTP NVM Technology on Altis Semiconductor's 130 nm and 180 nm RF CMOS Platform

SST's One-Time-Programmable (OTP) Antifuse NVM Technology Offers Industry-Leading Fast-read Capability and Smartbit™ Dynamic Programming for High Reliability

CHANDLER, Ariz., Feb. 22, 2016 /PRNewswire/ -- Microchip Technology Inc. (NASDAQ: MCHP), a leading provider of microcontroller, mixed-signal, analog and Flash-IP solutions, through its Silicon Storage Technology (SST) subsidiary, announced today that system-on-a-chip (SoC) designers now have access to SST's unique Smartbit™ based antifuse one-time-programmable (OTP) non-volatile memory (NVM) technology on the Altis Semiconductor 130 nm and 180 nm RF CMOS platforms. A long-established innovative specialty foundry, Altis Semiconductor's cost-effective and proven RF CMOS platform is qualified to automotive standards. SST's Smartbit OTP technology together with Altis RF CMOS process is ideal for wide temperature ranges and data retention in medical and implantable devices, automotive market applications, mobile and wireless applications and the general Internet of Things (IoT) market.



MICROCHIP

For more information on SST's Smartbit-based antifuse OTP NVM technology, visit <http://www.sst.com/technology/smartbit-otp-technology>.

"SST's Smartbit OTP technology provides SoC designers with an extremely high reliability solution," said Vipin Tiwari, director of marketing and business development at SST. "Since our engagement began over two years ago, Altis Semiconductor has been working with us to support our mutual customers with SST's unique Smartbit technology on both 130 nm and 180 nm nodes. We are excited about the opportunity to offer a competitive platform to our mutual customers."

SST's Smartbit OTP solution significantly reduces production costs by implementing a high density antifuse technology in standard logic CMOS without additional process steps. The Smartbit bit cell design and unique dynamic-programming technology maximize both device reliability and programming yield while offering faster programming and access time than other OTP technologies.

"Altis Semiconductor recognizes the need for highly reliable OTP memory in a small footprint," said Delphine Knaak, senior marketing manager for IT and partnership at Altis Semiconductor. "Our collaboration with SST has resulted in automotive qualified, Smartbit-based antifuse OTP technology that is already in production with leading international clients."

Follow Microchip:

- RSS Feed for Microchip Product News: <http://www.microchip.com/RSS-022216a>
- Twitter: <https://twitter.com/MicrochipTech>
- Facebook: <http://www.facebook.com/microchiptechnology>
- YouTube: <http://www.youtube.com/user/MicrochipTechnology>

About Silicon Storage Technology

Microchip Technology's SST subsidiary is a leading provider of embedded Flash technology. SST develops, designs, licenses and markets a diversified range of proprietary and patented SuperFlash memory technology solutions for the consumer, industrial, automotive and Internet of Things (IoT) markets. SST was founded in 1989, went public in 1995 (NASDAQ: SSTI), and was acquired by Microchip in April 2010. SST is now a wholly owned subsidiary of Microchip, and is headquartered in San Jose, Calif. For more information, visit the SST Web site at <http://www.sst.com>.

About Microchip Technology

Microchip Technology Inc. (NASDAQ: MCHP) is a leading provider of microcontroller, mixed-signal, analog and Flash-IP solutions, providing low-risk product development, lower total system cost and faster time to market for thousands of diverse customer applications worldwide. Headquartered in Chandler, Arizona, Microchip offers outstanding technical support along with dependable delivery and quality. For more information, visit the Microchip website at <http://www.microchip.com/homepage-022216a>.

About Altis Semiconductor

Altis Semiconductor is a leading innovative specialty foundry serving worldwide customers for high-volume high-quality semiconductor wafer manufacturing. Altis competitive value-added technology portfolio focuses on RF, analogue mixed-signal, embedded non-volatile memory and low-power, allowing customers to meet efficiently the challenges of the growing mobile connectivity, IoT, sensor and automotive markets. For more information, visit the Altis website at <http://www.altissemiconductor.com/en/>.

Note: The SST name and the SST logo are registered trademarks of Microchip Technology Inc. and Silicon Storage Technology Inc, as applicable, in the USA and other countries. All other trademarks mentioned herein are the property of their respective companies.

Tags / Keywords: OTP, NVM, RF CMOS, SoC, qualified automotive, Smartbit, antifuse, dynamic programming, IoT, sensors

Editorial Contact:

Sarah Broome
480-792-4386
sarah.broome@microchip.com

Reader Inquiries:

1-888-624-7435
<http://www.sst.com/technology/smartbit-otp-technology>

Logo - <https://photos.prnewswire.com/prnh/20141115/158835LOGO>

To view the original version on PR Newswire, visit: <http://www.prnewswire.com/news-releases/silicon-storage-technology-announces-availability-of-its-smartbit-otp-nvm-technology-on-altis-semiconductors-130-nm-and-180-nm-rf-cmos-platform-300223117.html>

SOURCE Microchip Technology Inc.