



# QuickLogic Investor Presentation Reach China Investor Conference

Brian Faith, CEO  
Sue Cheung, Ph.D., CFO

June 27, 2017

# Safe Harbor Statement

This presentation contains statements that are forward-looking including statements relating to the size of the total addressable market for our products and services, the compound annual growth rate for mobile market sectors, expectations relating to our new products, the time to market for various of our product innovations, expectations relating to our product innovations, our anticipated platform silicon roadmap and the expected timeline related to such roadmap, opportunities for our pipeline and our positioning for long-term, sustainable revenue growth, the benefits of our mobile-specific programmable logic to customers, the benefits of our solutions platforms, our plans with respect to new product revenue growth, our long-term target operating model, our projections related to our revenue, gross margin, expenses, operating income, net income and earnings per share. These forward-looking statements involve risks and uncertainties including but not limited to expectations relating to production targets for our New Products, revenue growth from our new products, our design activity and our ability to convert new design opportunities into customer activity, market acceptance of our customers' products and our expected results. In addition to U.S. GAAP financials, this presentation includes certain non-GAAP financial measures. These historical and forward-looking non-GAAP measures are in addition to, not a substitute for or superior to, measures of financial performance prepared in accordance with U.S. GAAP. QuickLogic's future results could differ materially from the results described in these forward-looking statements. These and other risk factors are detailed in QuickLogic's periodic reports and registration statements filed with the Securities and Exchange Commission. QuickLogic expressly disclaims any obligation to update or revise any forward-looking statements found herein to reflect any changes in Company expectations or results or any change in events.

# Investment Rationale

<p><b>Addressing Critical Needs</b></p>	<ul style="list-style-type: none"> <li>Enabling significantly longer battery life, more immersive user experience &amp; BOM integration</li> <li>Essential for Smartphones, Wearables, Hearables and IoT</li> </ul>
<p><b>Leveraging Partnership Model</b></p>	
<p><b>Creating Competitive Advantages</b></p>	<ul style="list-style-type: none"> <li>50+ patents in core IP</li> <li>Delivers flexibility and reduced R&amp;D costs for System on a Chip (SoC) vendors</li> <li>Enabling new form factors, new use cases</li> <li>Mobile-specific programmable logic enables hardware differentiation</li> </ul>
<p><b>Validating Technology</b></p>	
<p><b>Driving to Profitable Growth</b></p>	<ul style="list-style-type: none"> <li>Tier 1 Smartphone OEM Wearable program in User Test Stage; mass production next stage</li> <li>Entering new markets with eFPGA IP licensing opportunities</li> <li>Announced new initiatives for China-specific Wearable, Hearable, and IoT markets</li> <li>Lowering costs through strategic realignment; investments in software R&amp;D</li> </ul>

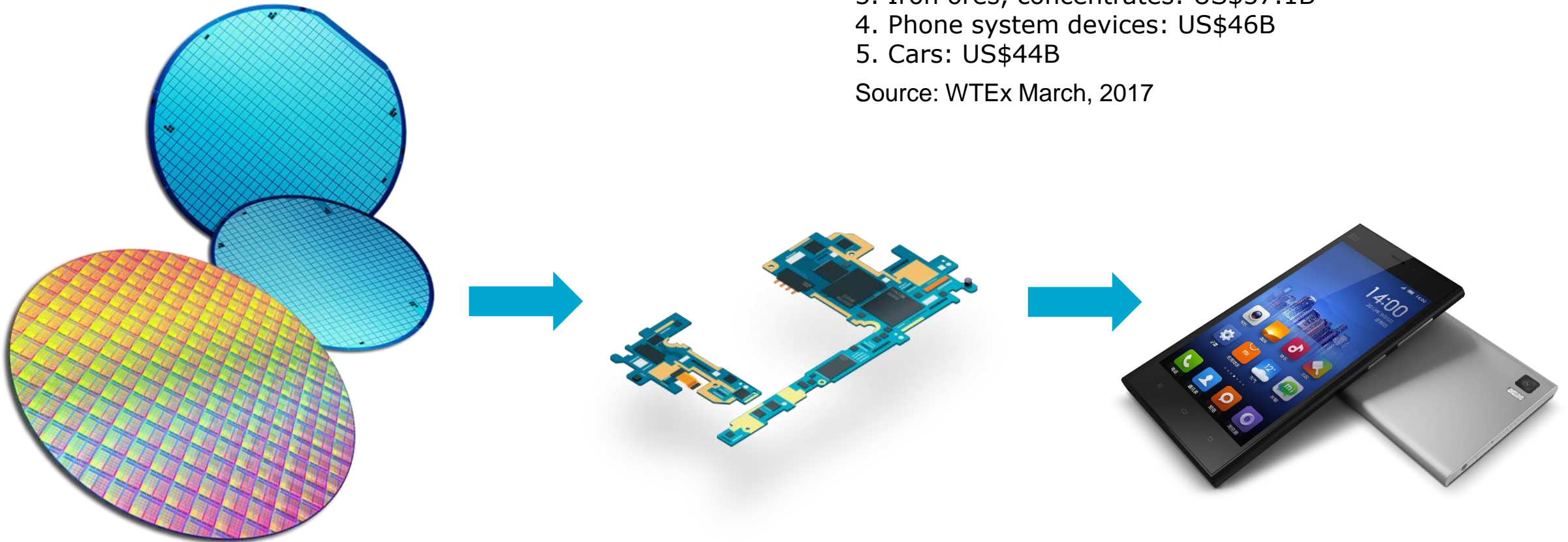
# What is a Semiconductor?

*“Chip” That Enables Most of the Functions in Electronic Systems,...*

*...and it is the #1 import into China.*

1. **Integrated circuits (Semiconductors): US\$228.6B**
2. Crude oil: US\$116.2B
3. Iron ores, concentrates: US\$57.1B
4. Phone system devices: US\$46B
5. Cars: US\$44B

Source: WTEEx March, 2017



# QuickLogic

Maximizing Battery Life for Immersive User Experiences in Smartphone, Wearable and IoT devices.



More intelligence



Reacts smartly to voice commands



Proactively interprets user context



**EOS™ S3**

Multicore sensor processing  
Programmable logic  
Sensor fusion algorithm libraries

**ArcticPro™**

eFPGA IP  
eFPGA compiler  
eFPGA place and route tools  
Foundry partners



Contextually aware services



Longer battery life



User flexibility

# Sensor Processing

## EOS™ S3

Multicore sensor processing  
Programmable logic  
Sensor fusion algorithm libraries

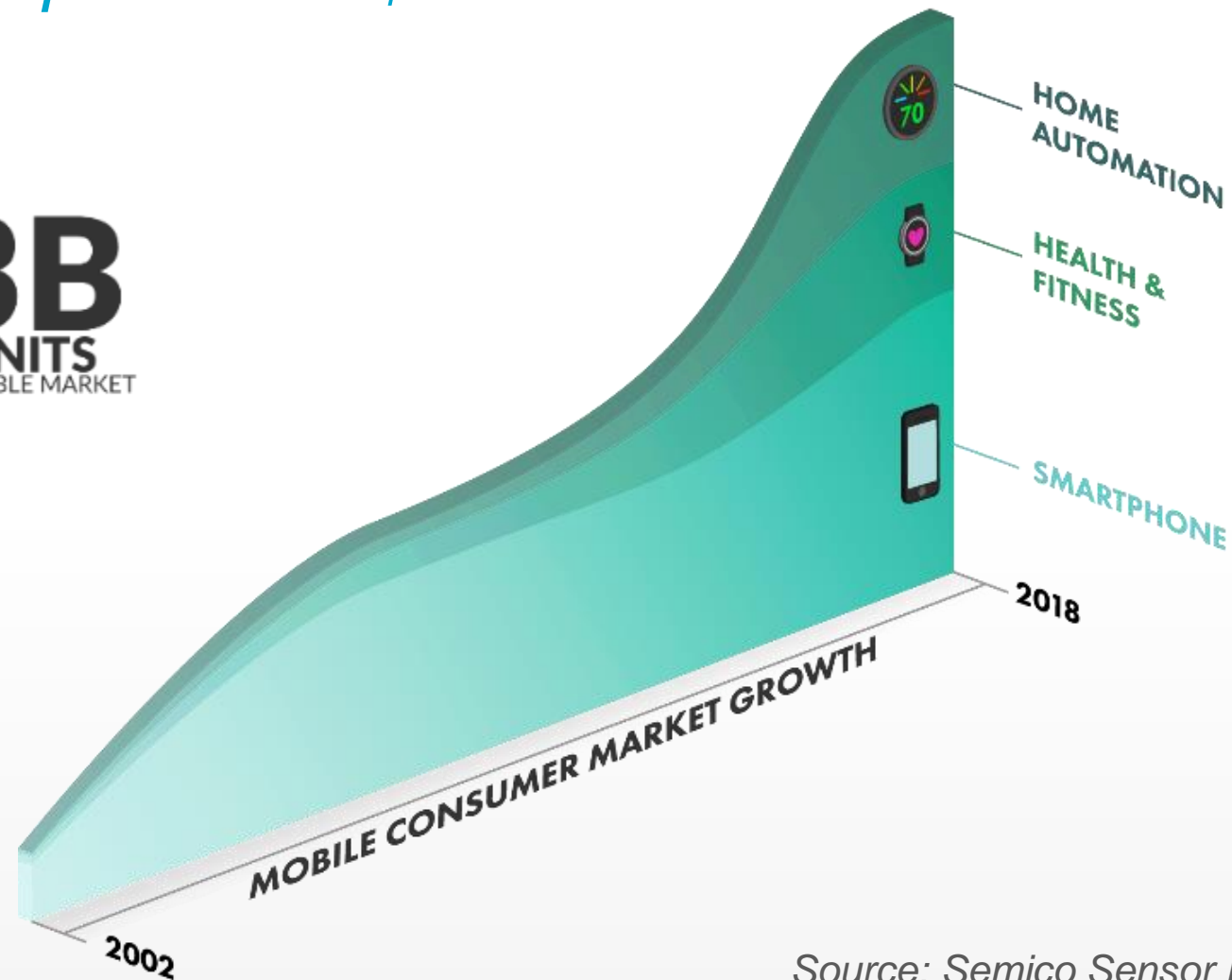
## ArcticPro™

eFPGA IP  
eFPGA compiler  
eFPGA place and route tools  
Foundry partners

# Serving High Growth, High Volume Markets

*Sensor Processing represents ~\$1.5B in 2018*

**~3B**  
UNITS  
AVAILABLE MARKET



Source: Semico Sensor Hub Report, June 2015

# The Immersive Experience Driving Mobile Internet Usage

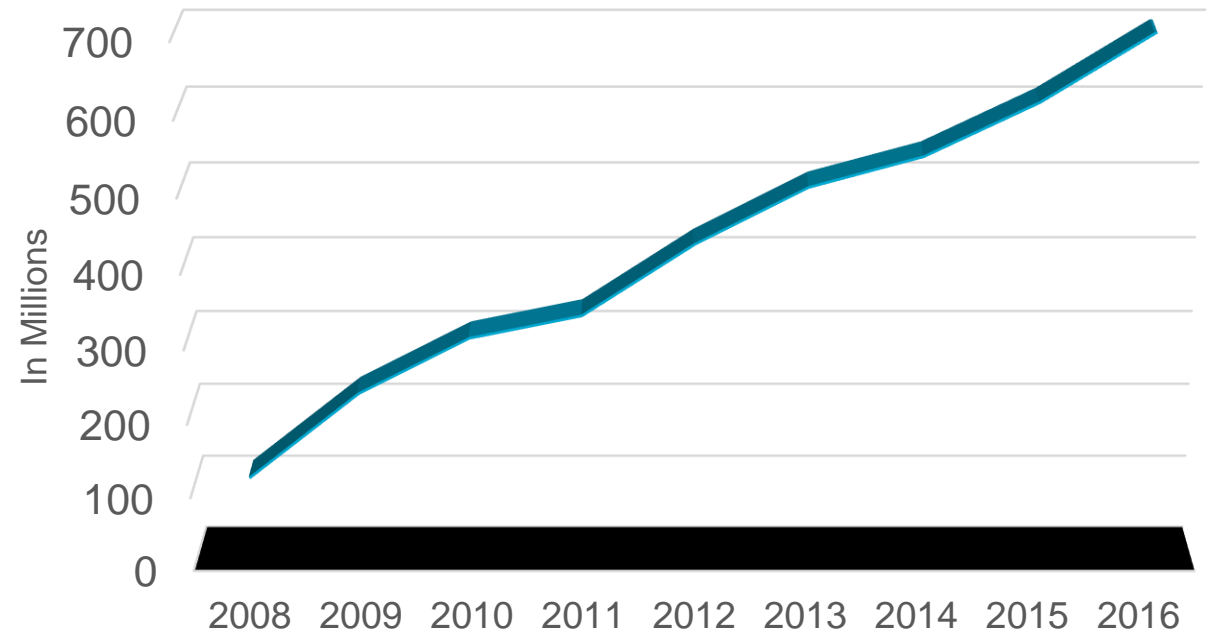
## *Always-On, Always Aware Trends*

### Market Drivers...

- More intelligence
- Contextually aware services
- Longer battery life



China Mobile Internet Users

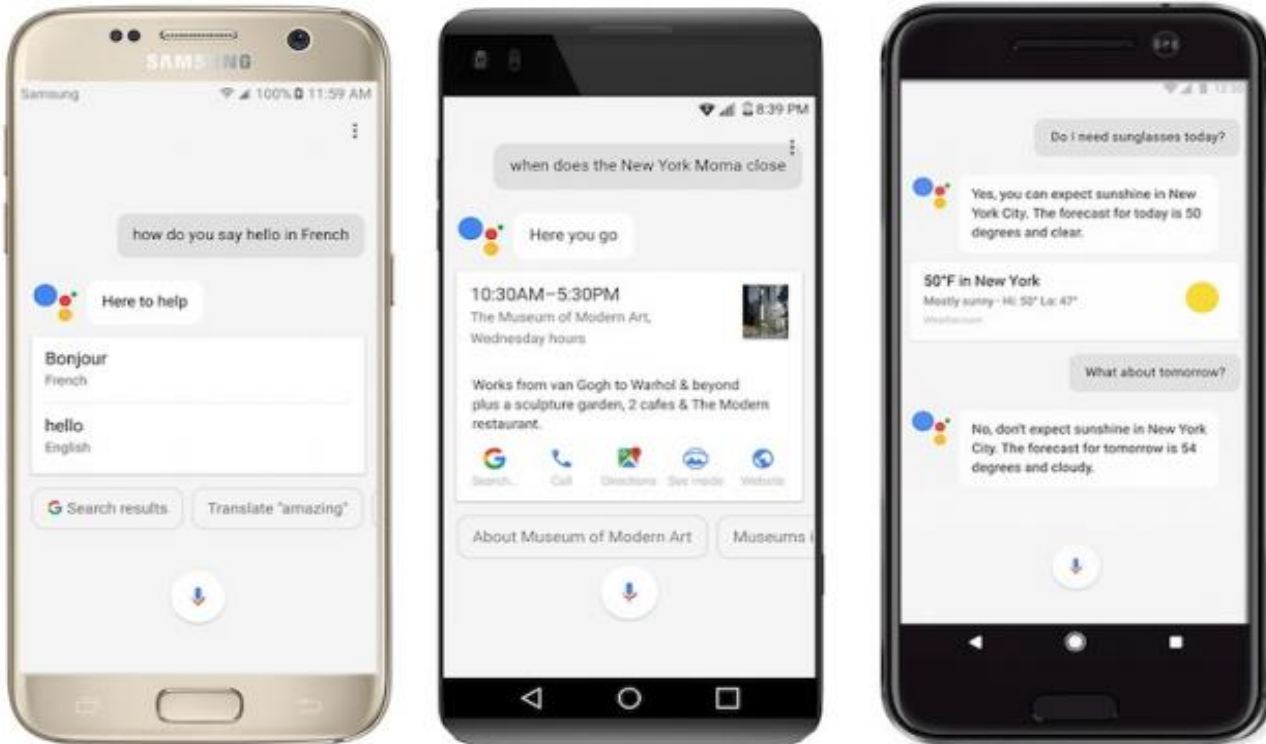


Source: Mary Meeker Internet Trends 2017 Code Conference

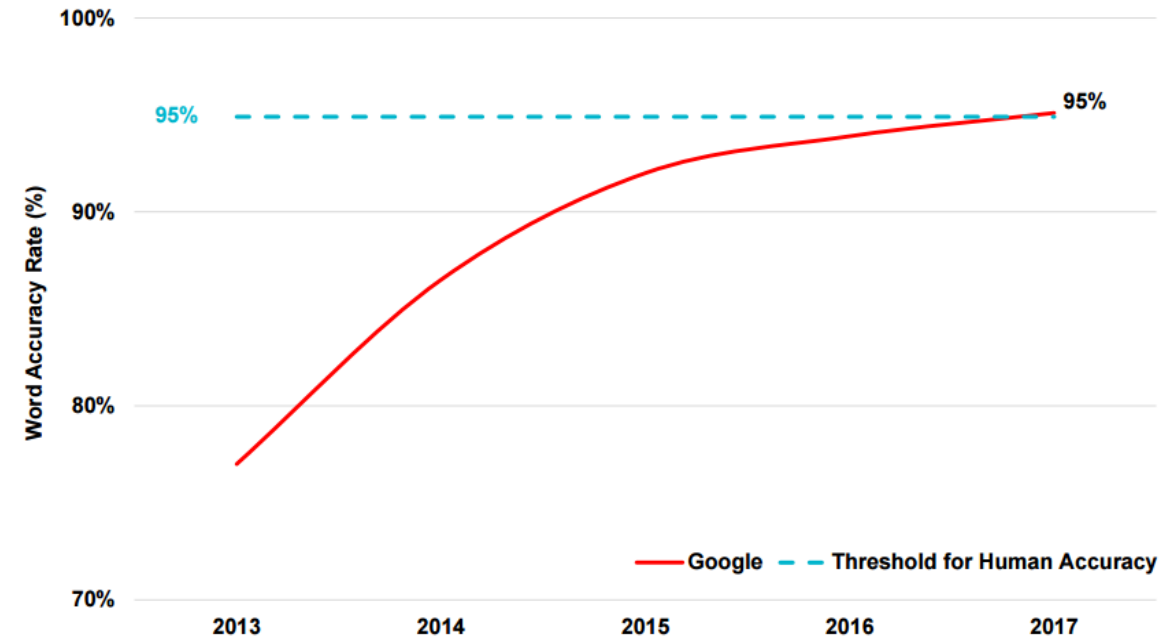
# The Growth of Voice Driven By Accuracy of User Experience

## *Voice Interfaces are Replacing Touch Interfaces*

20% of Mobile Queries Made via Voice, 5/16



**Google Machine Learning**  
Achieving Higher Word Accuracy, 2013-2017



Source: Mary Meeker / Kleiner Perkins  
Annual Internet Trends Report, June 2017

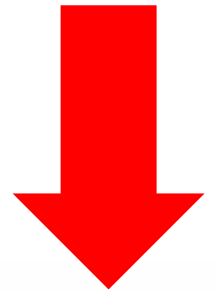
# Voice-Enabled Artificial Intelligence

*Focus of Both Large, and Emerging Technology Companies*



# Battery Life is the Challenge

Always-On Operation



Uses a LOT of Power

# Validation - Sensor Processors Solve the Power Problem



Introducing Pixel  
Phone by Google



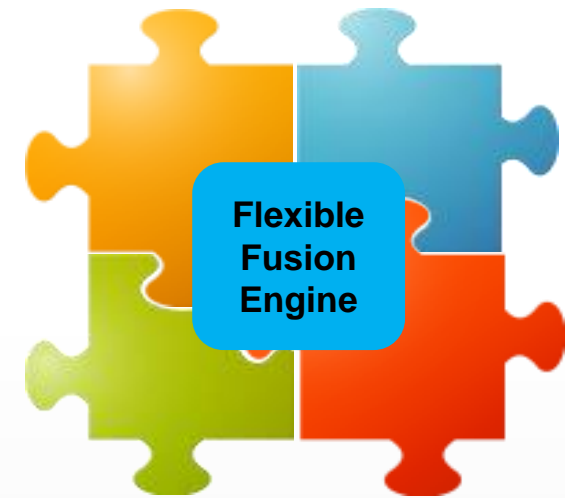
## Sensors

- Proximity / ALS
- Accelerometer / Gyrometer
- Magnetometer
- Pixel Imprint – Back-mounted fingerprint sensor for fast unlocking
- Barometer
- Hall effect sensor
- Android Sensor Hub
- Advanced x-axis haptics for sharper / defined response

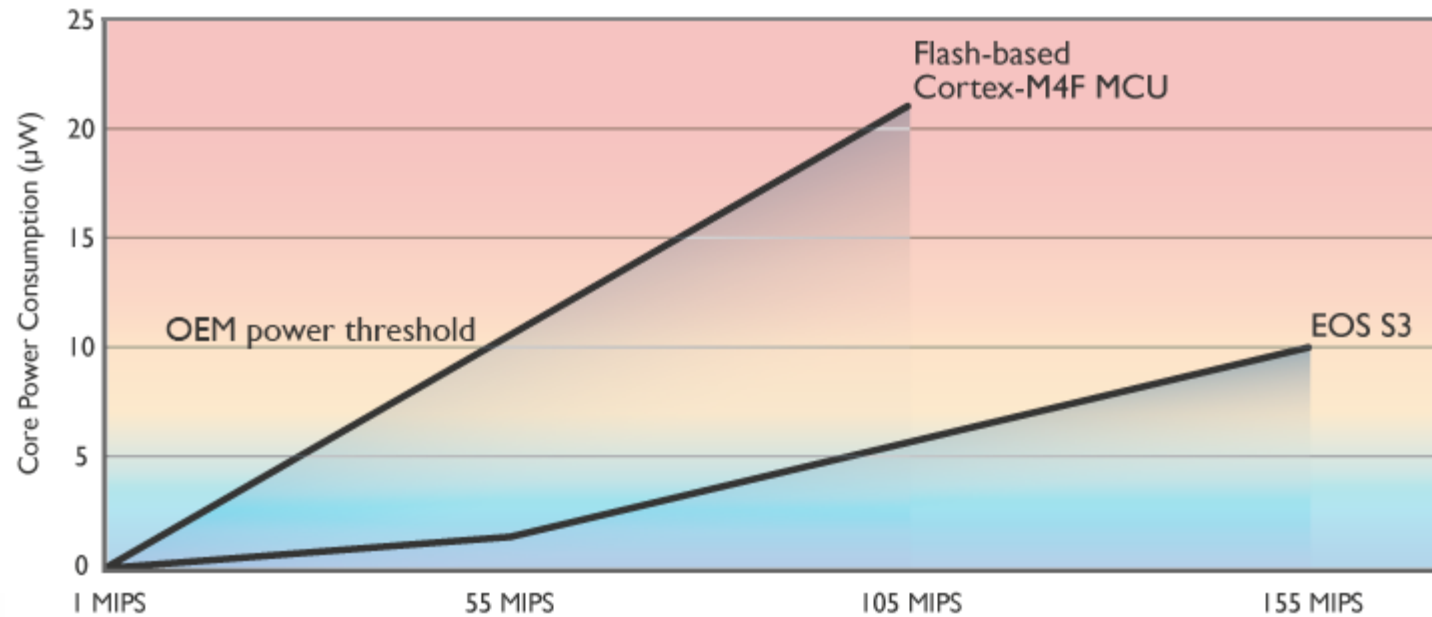
# Product and Technology Differentiation

## *Core IP to our Sensor Processing Strategy*

- Patent-pending Flexible Fusion Engine Sensor Processing Architecture for up to **70% lower power consumption**
- Patented Ultra-Low Power Programmable Logic enables **hardware differentiation**
- **50+ patents** in core IP



# The More Immersive the Experience, the Greater Our Advantage



EOS™

## Processing Requirements



Voice Trigger and Commands

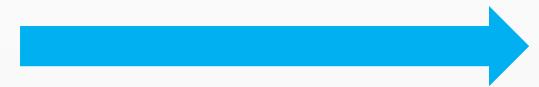


Motion-Compensated Heart Rate



Indoor Navigation

Increased Immersion



# Our EOS™ S3 is Lower Power than All Other Architectures



Discrete  
Sensor Hub

AP-Integrated  
MCU/DSP

Sensor with Integrated  
MCU

2017  
Market Share

524Mu, 24% of TAM

846Mu, 38% of TAM

830Mu, 38% of TAM



QuickLogic is 1/2 the power of other discrete sensor hubs and 1/10 the power of AP-integrated MCU/DSP sensor hubs

Source: Semico Sensor Hub Report, 2015

# Software Ecosystem Partnerships

## *Optimizing Immersive User Experience*

Ecosystem partnerships with industry leaders to **enable immersive consumer experience**



Leading supplier of Android-compliant sensor fusion algorithms for Chinese OEMs



AISpeech is a rapidly growing leading enterprise provider of Artificial Intelligence (AI) speech technology.



Leading supplier of deeply embedded voice recognition technology

# Sensor Processing Market Momentum

## *Implementing Core Sensor Processing Strategy*

- Made material progress on EOS™ S3 design for a wearable product with a well-known app company, which has selected an ODM for targeted production ramp in late 2017
- Entered design in stage of engagement process with 2<sup>nd</sup> large app company that is targeting EOS S3 for a new smart hearable device
- Announced the availability of new sensor fusion software from developer CyweeMotion that supports the Android "Nougat" operating system for smartphones
- Announced availability of Acoustic Echo Cancellation (AEC) technology for expanded use cases of always-on voice applications
- Initiated top-tier voice-enabled IoT engagement for EOS S3
- Built upon reaching user-testing stage with Tier-one smartphone OEM wearable program

# Sensor Processing Market Momentum

## *Recent Announcements for China-specific Strategy*

- Investment in China Market Development
  - Establishment of Wholly Foreign Owned Enterprise in Shanghai
- Technology Ecosystem Partners
  - Collaboration with AI Speech to bring cloud-based Artificial Intelligence-based digital assistant to battery-powered IoT devices
- ODM Partnerships
  - Qiwo Smartlink will use QuickLogic's EOS S3 as host processor for upcoming Bluetooth headset product
  - Janyun will use QuickLogic's EOS S3 as host processor for voice-enabled GPS Smartwatch product

# Established Wholly Foreign Owned Enterprise in China

## *Demonstrates Commitment to China Market*

- Located in Shanghai
- Enables us to address smartphone, wearable, hearable, and other voice-enabled IoT products with local team
- Target to accelerate design activity for QuickLogic products in China



June 19, 2017

### **QuickLogic Establishes Wholly Foreign Owned Enterprise in China**

#### **Investment Demonstrates Commitment to the China Mobile Market**

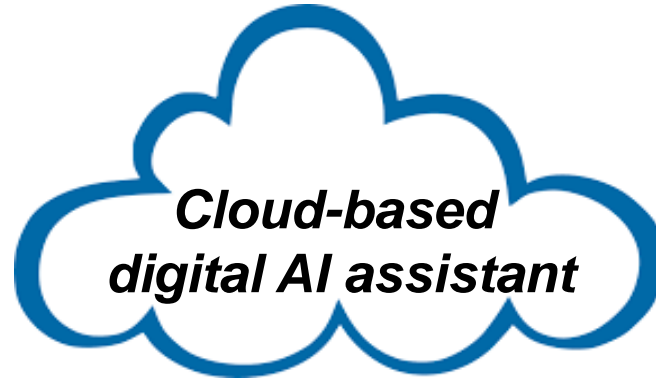
SUNNYVALE, Calif., June 19, 2017 (GLOBE NEWSWIRE) -- QuickLogic Corporation (NASDAQ:QUIK), an innovator and developer of ultra-low power programmable sensor processing solutions, display bridges, FPGAs, and embedded FPGA (eFPGA) IP, established a Wholly Foreign Owned Enterprise (WFOE) in Shanghai, China to accelerate the company's expansion in China and other Asian markets. This complements the company's existing research and development and support center in India and the recently established eFPGA IP support center in Taiwan.

"By using the WFOE structure for our new Chinese venture we are able to maintain 100% control and ownership of the new enterprise, and through that, optimize the focus on our goals and protection of our IP," said Brian Faith, president and CEO of QuickLogic Corporation. "With eight of the top ten smartphone companies and a rapidly expanding market for wearable and voice-enabled IoT products, China represents a huge opportunity for QuickLogic. We believe that having a WFOE in Shanghai will enable us to address these opportunities more effectively and accelerate our numerous EOS™ S3 and eFPGA engagements with major Chinese OEMs."

#### **About QuickLogic**

QuickLogic Corporation (NASDAQ:QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithm solutions for always-on voice and sensor processing. The Company's embedded FPGA initiative also enables SoC designers to easily implement post production changes, and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, please visit [www.quicklogic.com](http://www.quicklogic.com).

# Qiwo Smartlink Technology Chooses EOS S3 Multi-Core SoC for Voice-Enabled Bluetooth Headset Designs



EOS S3 & **sensory**  
Voice Technology

Enabled by **AISPEECH**



“The **QuickLogic EOS S3 SoC** provided us the **lowest possible power consumption** for new voice enabled headsets,” said Fengko Gao, CEO of Qiwo. “With its multi-core design, **integrated voice technology** and inherent **flexibility to address multiple use cases**, we look forward to incorporating the EOS S3 solution in future wearable and IoT designs. We believe this strategy will help us accelerate new product development, address multiple use cases from a single platform and leverage the broad market penetration that Qihoo 360 has established in China.”

# Janyun Chooses EOS S3 Sensor Processing Platform for Voice-Enabled GPS Smartwatch

- New design complements Janyun's successful line of wearable IoT products & cloud applications
- EOS S3 platform provides host processor, sensor processor, voice interface, GPS interface and display driver in the new design



“The QuickLogic EOS S3 SoC is an ***ideal solution for our wearable and IoT ODM designs***,” said Saicheng Tang, CEO of Janyun. “The EOS S3 platform provides us with a single chip, multi-core solution that optimizes our designs for cost, ultra-low power consumption, and design flexibility that enables us to support multiple end customer use cases with a single platform. We view these capabilities as ***essential to our success in the development of our ODM business model***.”

# eFPGA IP Licensing

## EOS™ S3

Multicore sensor  
processing  
Programmable logic  
Sensor fusion  
algorithm libraries

## ArcticPro™

eFPGA IP  
eFPGA compiler  
eFPGA place and  
route tools  
Foundry partners

# Entering New Markets

## *Embedded FPGA IP Licensing Positioning*



- Targets significant financial potential
  - \$10M+ potential annual licensing revenue with upside on royalty revenue in 2+ years
- Creates new, very high gross margin manufacturing licenses revenue streams
- Delivers flexibility and reduced R&D costs for SoC vendors addressing *fragmented* IoT markets

The embedded semiconductor intellectual property (IP) market is expected to grow from \$3.09B to over \$7B by 2022, according to Markets&Markets.

# eFPGA Benefits

## *Adding Post-Fabrication Flexibility to SoCs*

- Adds Flexibility – Enables complex functionality
- Ultra-Low Power Consumption – Power optimized
- Increased Performance – Eliminates chip-to-chip delays
- Higher Revenues – Enables multiple product variants
- Greater Profits – Closer match to market needs
- Lower R&D Costs – Reduces development time & cost
- Faster Time to Market – Supports post-fabrication changes

### SEMICONDUCTOR ENGINEERING

Home > System-Level Design > Embedded FPGAs Come Of Age

SYSTEM-LEVEL DESIGN

## Embedded FPGAs Come Of Age

f 17 t in 70 G+ 24

*These devices are gaining in popularity for more critical functions as chip and system designs become more heterogeneous.*

FEBRUARY 27TH, 2017 - BY: ANN STEFFORA MUTSCHLER



FPGAs increasingly are being viewed as a critical component in heterogeneous designs, ratcheting up their stature and the amount of attention being given to programmable devices.

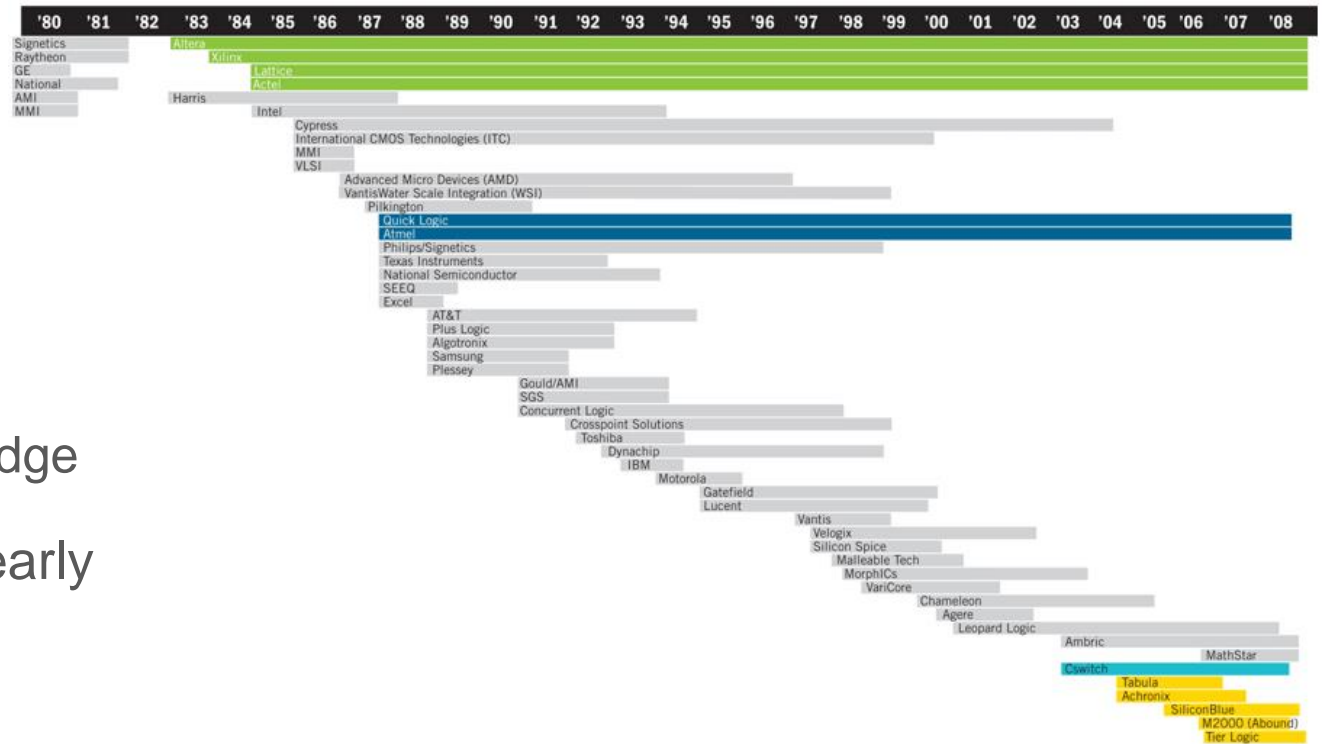
Once relegated to test chips that ultimately would be replaced by lower-power and higher-performance ASICs if volumes were sufficient, FPGAs have come a long way. Over the last 20 years programmable devices have moved steadily up the food chain from glue logic to co-processors, and they have been utilized in a variety of high-performance, mission-critical applications from data centers to supercomputers.

# QuickLogic is One of Only Four FPGA Companies with Worldwide Presence Still Operating Independently

- Strong growth forecast for FPGA market
- Significant M&A in this space
  - Microsemi purchased Actel
  - Microchip purchased Atmel
  - Intel purchased Altera
  - Lattice pending acquisition by Canyon Bridge
- We are *uniquely positioned* to leverage nearly 30 years of FPGA technology and product investment into our new licensing model

## History of PLD(FPGA) Startups

Source: EE Times



# Secured Two Top-tier eFPGA Foundries

## 1<sup>st</sup> eFPGA IP for GLOBALFOUNDRIES' 22FDX®

- ArcticPro™, **first** eFPGA announced for **new** GLOBALFOUNDRIES' 22FDX® (FDSOI) process
- Supports 65nm and 40nm leveraging existing technology and foundry relationship with GLOBALFOUNDRIES
- Gained early access to GLOBALFOUNDRIES' 22nm FD-SOI Process by joining its FDXcelerator™ Partner Program
- Second top-tier foundry to port, and market for licensing, that expands the availability to semiconductor companies and OEMs developing new designs targeting this foundry's high volume 40nm fabrication process
- Accelerates QuickLogic's access to leading edge process technology for our own silicon development



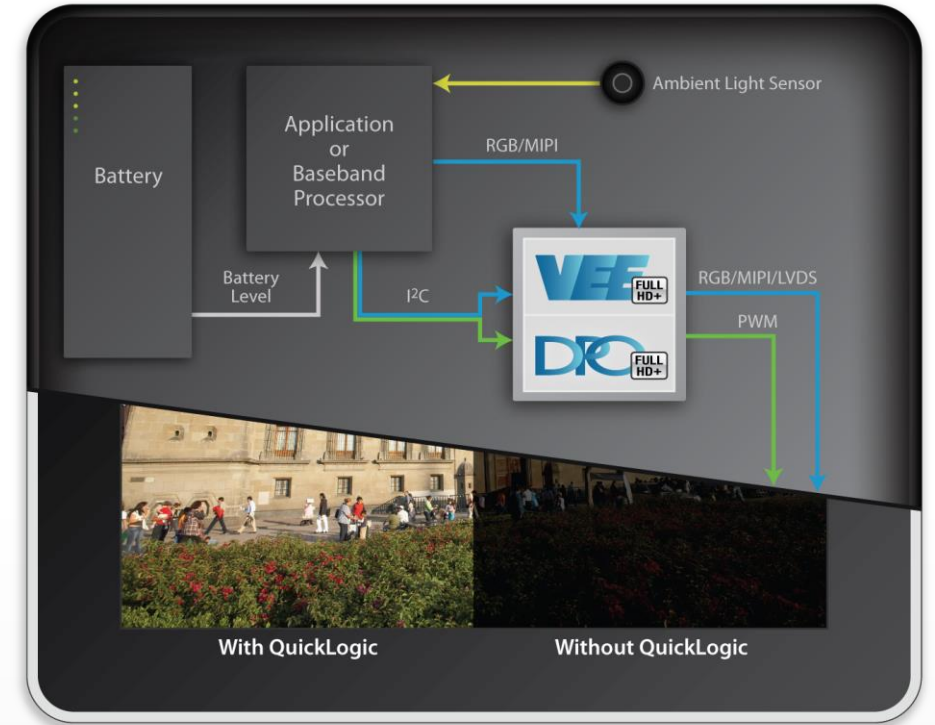
Un-named, 2<sup>nd</sup>  
Top-Tier  
Foundry

# Embedded FPGA (eFPGA) Market Momentum

- Established eFPGA IP Support Center in Taiwan to provide fast, local support for licensees, foundries and ecosystem partners
- Taped-out the test chip with a second top-tier foundry company and initiated customer engagements
- Presented at the 2017 SMIC Advanced Technology Workshops in Shanghai, Santa Clara & Hsinchu, Taiwan
- Released new Aurora eFPGA software tools, supporting eFPGA design implementation from RTL through place and route
- QuickLogic CTO Tim Saxe delivered the keynote address at the IoT Summit 2017 in Santa Clara, CA
- Increased number of significant ArcticPro™ eFPGA engagements

# Display Bridge Products – Maintaining Momentum

- Continue to ship into consumer tablet market
- Design wins expanding across number of end markets, including tablet, consumer, and automotive
- Expect to continue display bridge revenue well into 2018 and beyond with large OEMs



# QuickLogic Leadership

## Executives

### Brian Faith

*Chief Executive Officer, Director*

- Joined in 1996
- CEO in 2016
- VP of Worldwide Marketing/ VP of Worldwide Sales & Marketing 2008-2016
- Rising managerial & executive positions, including engineering, product line management, marketing and sales

### Sue Cheung, Ph.D.

*Chief Financial Officer*

- Joined in 2007
- CFO in 2017, VP of Finance in 2016
- Principal Accounting Officer, Corp. Controller & Asst. Controller 2007-2016
- Sr. Accounting management positions at Dell SonicWALL, VeriFone and other publicly traded and privately held companies
- CPA and PH.D. in Business Administration

### Timothy Saxe, Ph.D.

*Chief Technology Officer & SVP Engineering*

- Joined in 2001
- CTO in 2008, SVP of Eng. in 2016
- Rising executive positions including VP of Eng. and VP of Software Eng.
- VP of FLASH Engineering at Actel Corp. and founder/CEO of semiconductor mfg. division at GateField Corp. (Zycad)
- Doctorate in Electrical Engineering

### Rajiv Jain

*VP Worldwide Operations*

- Joined in 1992
- VP Worldwide Ops in 2014
- Sr. Dir. of Operations & Development Eng., Sr. Dir. of System Solutions & Process Technology, Dir. of Process Technology, and Sr. Process Technologist 1992-2014
- Sr. Engineering positions at National Semiconductor and Monolithic Memories

## Board of Directors

### E.Thomas Hart

*Chairman*

- QuickLogic CEO 1994-2009
- VP & GM of Advanced Networking Division at National Semiconductor Corp.
- Senior Manager, Motorola, Inc.

### Michael R. Farese

*Director*

- 35 years in executive roles in telecomm & semiconductor industry including Antenna29; Entropic Comm. Inc.; BitWave Semicon, Palm; WJ Comm; Tropian Inc.; Motorola Corp.; Ericsson Inc.; Nokia Corp.; ITT Corp.; AT&T Corp.; and Bell Labs

### Arturo Krueger

*Director*

- Over 40 years leadership in systems architecture, semiconductor design & development, operations and marketing
- Executive roles at Motorola, Inc. semiconductor products; Director, Marvell Technology Group Ltd

### Andrew J. Pease

*Director*

- QuickLogic CEO/President 2009-2016, VP Worldwide Sales 2006-2009
- Executive roles at Broadcom Corp.; Synticity, Inc.
- Various sales positions at Advanced Micro Devices; Integrated Systems Inc.; and Vantis Corp.

### Daniel A. Rabinovitsj

*Director*

- COO, Ruckus Wireless, Inc.
- Executive roles at Qualcomm Atheros, Inc.; Atheros Communications; NXP Semiconductors; ST Ericsson; and Silicon Labs

### Christine Russell

*Director*

- CFO, UniPixel, Inc., Vendavo, Inc.,
- CFO positions at Evans Analytical Group; Virage Logic Corp.; OuterBay; Ceva, Inc.; and Persistence Software, Inc.

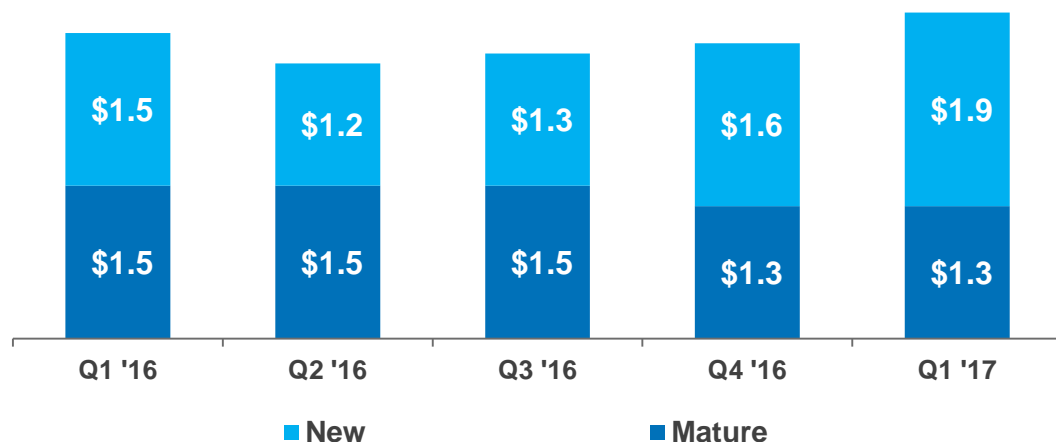
### Gary H. Tauss

*Director*

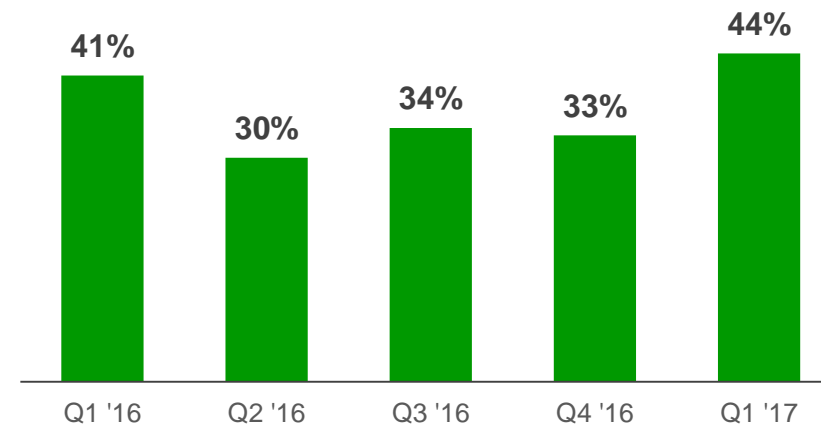
- Exec. Director & CEO, BizTech
- Executive roles at Mobidia Technology, Inc.; InfiniRoute Networks, Inc.; LongBoard, Inc.; and TollBridge Technologies

# Historical Quarterly Highlights

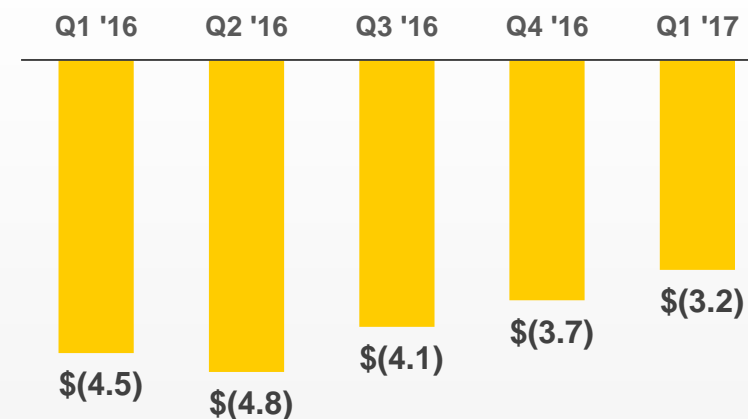
**Revenue**  
(\$ in Ms)



**Non-GAAP Gross Margin**



**Non-GAAP Net Loss\***  
(\$ in Ms)



**Balance Sheet Data** as of 4/2/2017  
(\$ in millions)

Total Cash	\$26.7
Bank Debt - 3.75% Int.	\$6.0

# Q2 2017 Financial Guidance

<b>Non-GAAP</b>	<b>Q4'16</b>	<b>Q1'17</b>	<b>Q2'17</b>
<i>(\$ in millions, except noted)</i>	<b>Actual</b>	<b>Actual</b>	<b>Guidance</b>
New Product Revenue	\$ 1.6	\$ 1.9	\$ 1.8
Mature Product Revenue	\$ 1.3	\$ 1.3	\$ 1.4
<b>Total Revenue</b>	<b>\$ 2.9</b>	<b>\$ 3.2</b>	<b>\$3.2 +/- 10%</b>
Non-GAAP Gross Margin	33%	44%	44%+/-3%
Net Loss	\$ (3.7)	\$ (3.2)	\$ (3.3)
Net Loss Per Share	\$ 0.05	\$ 0.05	\$0.04

- **Q2 2017 Cash usage expected to be between \$3.8M and \$4.2M (net of financing cost)**
- **Revenue to ramp in 2H 2017 with new customers and programs**

Actual results may vary significantly due to things that are beyond the company's control, such as schedule variations from customers, schedule changes and projected production start dates could push or pull shipments.

# Target Operating Model

Targets	Mid Term (2 year)	Long Term (>2 year)
Revenue Growth	>50%	>50%
Non-GAAP Gross Margin	45 - 50%	>50%
Non-GAAP Operating Margin	0 - 10 %	10 - 15%+

Reaffirms Non-GAAP break-even revenue rate between \$9.0M and \$11.0M/qtr. with a 50% gross margin

**NOTE:** These projections are subject to a number of assumptions, risks, uncertainties and other factors that may cause our actual results to differ materially from such projections

# Investment Highlights

<p><b>Addressing Critical Needs</b></p>	<ul style="list-style-type: none"> <li>Enabling significantly longer battery life, more immersive user experience &amp; BOM integration</li> <li>Essential for Smartphones, Wearables, Hearables and IoT</li> </ul>
<p><b>Leveraging Partnership Model</b></p>	
<p><b>Creating Competitive Advantages</b></p>	<ul style="list-style-type: none"> <li>50+ patents in core IP</li> <li>Delivers flexibility and reduced R&amp;D costs for System on a Chip (SoC) vendors</li> <li>Enabling new form factors, new use cases</li> <li>Mobile-specific programmable logic enables hardware differentiation</li> </ul>
<p><b>Validating Technology</b></p>	
<p><b>Driving to Profitable Growth</b></p>	<ul style="list-style-type: none"> <li>Tier 1 Smartphone OEM Wearable program in User Test Stage; mass production next stage</li> <li>Entering new markets with eFPGA IP licensing opportunities</li> <li>Announced new initiatives for China-specific Wearable, Hearable, and IoT markets</li> <li>Lowering costs by through strategic realignment; investments in software R&amp;D</li> </ul>



# Appendix

# Non-GAAP Measures

QuickLogic reports financial information in accordance with GAAP, but believes that non-GAAP financial measures are helpful in evaluating its operating results and comparing its performance to comparable companies. Accordingly, the Company excludes charges related to stock-based compensation, restructuring, the effect of the write-off of long-lived assets and the tax effect on other comprehensive income in calculating non-GAAP (i) income (loss) from operations, (ii) net income (loss), (iii) net income (loss) per share, and (iv) gross margin percentage. The Company provides this non-GAAP information to enable investors to evaluate its operating results in a manner similar to how the Company analyzes its operating results and to provide consistency and comparability with similar companies in the Company's industry. Management uses the non-GAAP measures, which exclude gains, losses and other charges that are considered by management to be outside of the Company's core operating results, internally to evaluate its operating performance against results in prior periods and its operating plans and forecasts. In addition, the non-GAAP measures are used to plan for the Company's future periods, and serve as a basis for the allocation of the Company's resources, management of operations and the measurement of profit-dependent cash and equity compensation paid to employees and executive officers. Investors should note, however, that the non-GAAP financial measures used by QuickLogic may not be the same non-GAAP financial measures, and may not be calculated in the same manner, as that of other companies. QuickLogic does not itself, nor does it suggest that investors should, consider such non-GAAP financial measures alone or as a substitute for financial information prepared in accordance with GAAP. A reconciliation of GAAP financial measures to non-GAAP financial measures is included in the financial statements portion of this press release. Investors are encouraged to review the related GAAP financial measures and the reconciliation of non-GAAP financial measures with their most directly comparable GAAP financial measures.

# P&L – Non-GAAP

Non-GAAP Results Millions (except for EPS)	Q1'16 Actual	Q2'16 Actual	Q3'16 Actual	Q4'16 Actual	FY 2016 Actual	Q1'17 Actual
New Product Revenue	\$1.5	\$1.2	\$1.3	\$1.6	<b>\$5.6</b>	\$1.9
Mature Revenue	\$1.5	\$1.5	\$1.5	\$1.4	<b>\$5.8</b>	\$1.3
Total Revenue	\$3.0	\$2.7	\$2.8	\$3.0	<b>\$11.4</b>	\$3.2
Gross Margin %	41%	30%	34%	33%	<b>35%</b>	44%
Research & Development	\$3.2	\$3.2	\$2.6	\$2.3	<b>\$11.3</b>	\$2.3
SG&A	\$2.5	\$2.4	\$2.4	\$2.3	<b>\$9.5</b>	\$2.3
Total Operating Expense	\$5.6	\$5.6	\$5.0	\$4.6	<b>\$20.8</b>	\$4.6
Operating Income (Loss)	(\$4.5)	(\$4.8)	(\$4.1)	(\$3.6)	<b>(\$16.9)</b>	(\$3.2)
Net Income (Loss)	(\$4.6)	(\$4.8)	(\$4.1)	(\$3.7)	<b>(\$17.2)</b>	(\$3.2)
EPS	(\$0.08)	(\$0.07)	(\$0.06)	(\$0.05)	<b>(\$0.26)</b>	(\$0.05)

# GAAP to Non-GAAP Reconciliation

	Q1'16	Q2'16	Q3'16	Q4'16	FY 2016	Q1'17
(In \$ '000)	Actual	Actual	Actual	Actual	Actual	Actual
Non-GAAP Operating Loss	\$ (4,422)	\$ (4,747)	\$ (4,072)	\$ (3,610)	\$ (16,851)	\$ (3,150)
Stock-based Compensation	(562)	(439)	(457)	(125)	(1,583)	(318)
Restructuring Costs	-	-	-	-	-	-
FA impairment and/or write-off	-	(312)	(39)	(17)	(368)	-
GAAP Operating Loss	\$ (4,984)	\$ (5,498)	\$ (4,568)	\$ (3,752)	\$ (18,802)	\$ (3,468)

Non-GAAP Net Loss	\$ (4,531)	\$ (4,823)	\$ (4,127)	\$ (3,716)	\$ (17,197)	\$ (3,247)
Adjustments:						
Stock-based Compensation	(562)	(439)	(457)	(125)	(1,583)	(318)
FA impairment and/or write-off	-	(312)	(39)	(17)	(368)	-
GAAP Net Loss	\$ (5,093)	\$ (5,574)	\$ (4,623)	\$ (3,858)	\$ (19,148)	\$ (3,565)