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Intel Adds to Portfolio of FPGA Programmable Acceleration Cards to Speed Up Data Center Computing

SANTA CLARA, Calif.--(BUSINESS WIRE)-- **What's New:** Intel today extended its field programmable gate array (FPGA) acceleration platform portfolio with the addition of the new [Intel® Programmable Acceleration Card \(PAC\) with Intel® Stratix® 10 SX FPGA](#), Intel's most powerful FPGA. This high-bandwidth card leverages the Acceleration Stack for Intel® Xeon® CPU with FPGAs, providing data center developers a robust platform to deploy FPGA-based accelerated workloads. Hewlett Packard Enterprise* will be the first OEM to incorporate the Intel PAC with Stratix 10 SX FPGA along with the Intel Acceleration Stack for Intel Xeon Scalable processor with FPGAs into its server offering.

This press release features multimedia. View the full release here:

<https://www.businesswire.com/news/home/20180925005338/en/>

Intel introduced the Intel Programmable Acceleration Card (PAC) with Intel Stratix 10 SX FPGA in September 2018. The card leverages the Acceleration Stack for Intel Xeon CPU with FPGAs, providing data center developers a robust platform to deploy FPGA-based accelerated workloads. (Credit: Intel Corporation)

"We're seeing a growing market for FPGA-based accelerators, and with Intel's new FPGA solution, more

developers – no matter their expertise – can adopt the tool and benefit from workload acceleration. We plan to use the Intel Stratix 10 PAC and acceleration stack in our offerings to enable customers to easily manage complex, emerging workloads."

-- Bill Mannel, vice president and general manager, HPC and AI Group, HPE

What It Does: Like the previously announced Intel PAC with Intel® Arria® 10 FPGA, this new Intel PAC with Stratix 10 SX FPGA supports an ecosystem of design partners that delivers IP to accelerate a wide range of application workloads. The Intel PAC with Stratix 10 SX FPGA is a larger form factor card built for inline processing and memory-intensive workloads, like streaming analytics and video transcoding. While the smaller form factor Intel PAC with Arria 10 FPGA is ideal for backtesting, data base acceleration and image processing workloads.

Why It's Important: As the demands for big data and artificial intelligence (AI) increase, the reprogrammable technology of the FPGA meets the processing requirements and changing workloads of data center applications. With reconfigurable logic, memory and digital signal processing blocks, FPGAs can be programmed to execute any type of function with high throughput and real-time performance, making them ideal for many critical enterprise and cloud applications.

The acceleration stack for Intel Xeon CPU with Intel FPGAs works with industry-leading OS,

virtualization and orchestration software partners, providing a common interface for software developers to get faster time to revenue, simplified management and access to a growing ecosystem of acceleration workloads.

What the Solution Includes:

- Intel-validated Intel Programmable Acceleration Card (PAC) with Intel Stratix 10 SX FPGA.
- Production-grade FPGA Interface Manager (FIM) to which Intel and partner AFUs are connected.
- Acceleration Stack for Intel Xeon CPU with FPGAs, including a common set of APIs and open-source drivers that work seamlessly with industry-leading OS, virtualization and orchestration software across the portfolio of Intel programmable acceleration cards.
- Support for native, network-attached workloads; initial partners including [Adaptive Microware](#)* and [Megh Computing](#)*, with more to come.
- Workloads available through acceleration workload storefront for ease of evaluation.

More Context: [Programmable Solutions Group News](#)

The Small Print: Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Performance varies depending on system configuration. No computer system can be absolutely secure. Check with your system manufacturer or retailer or learn more at [intel.com](#).

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