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Meet QuickLogic at Space Tech Expo Europe to Discuss Rad-Hard eFPGA IP for Space Missions

- Silicon-proven eFPGA IP brings post-launch flexibility to mission-critical SoCs. Visit Stand R19, Hall 6

SAN JOSE, Calif., Nov. 4, 2026 /PRNewswire/ -- QuickLogic® Corporation (NASDAQ: QUIK), a developer of embedded FPGA (eFPGA) Hard IP, Radiation Hardened and Antifuse FPGAs, and ruggedized programmable logic solutions, will exhibit at Space Tech Expo Europe in Bremen, Germany, November 17–19, 2026, at Stand R19 in Hall 6.

QuickLogic will highlight its silicon-proven, radiation-hardened eFPGA IP, which lets aerospace and defense engineers build reprogrammable logic directly into Systems-on-Chips (SoCs). With the Australis™ eFPGA IP Generator, designers can fit logic to a mission's requirements and update it after deployment. This extends mission life without the cost of a full hardware redesign.

Key capabilities for space and defense:

- **Radiation-Hardened by Design (RHBD):** Built to withstand Total Ionizing Dose (TID) and Single Event Effects (SEE).
- **Customizable eFPGA IP:** Logic density and performance can be scaled across multiple process nodes and foundries.
- **Flexible implementation:** Available as an embedded eFPGA core or as a custom rad-hard FPGA.
- **EAR99 export classification:** Makes procurement simpler for international space and defense programs.

Visit QuickLogic at Stand R19, Hall 6, to see how customizableeFPGA technology can make next-generation space systems more reliable and adaptable.

About QuickLogic

QuickLogic is a fabless semiconductor company specializing in embedded FPGA (eFPGA) Hard IP, Radiation Hardened and Antifuse FPGAs, and ruggedized programmable logic solutions. QuickLogic's unique approach combines cutting-edge technology with open-source tools to deliver highly customizable low-power solutions for aerospace and defense, industrial, computing, and consumer markets. For more information, visit

www.quicklogic.com.

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