QuickLogic

Flexible Silicon. Proven IP. Expanding Markets.



November 2026

* All data in this presentation, including forecasts and projections, is as of November 10, 2026 (as disclosed on our third fiscal quarter 2026 conference call)

Safe Harbor Statement

This presentation contains forward-looking statements within the meaning of the Private Securities Litigation Reform Act of 1995. These forward-looking statements include, without limitation, statements regarding our future profitability, revenue growth and cash flows, expectations regarding our future business and statements regarding the timing, milestones, and payments related to our government contracts, statements regarding expected contracts and the expected magnitude of such contracts, statements regarding the likelihood and timing of storefront opportunities and statements regarding expected adoption rates and/or orders by our customers, and actual results may differ due to a variety of factors including: delays in the market acceptance of the Company's products; the ability to convert design opportunities into customer revenue; our ability to replace revenue from end-of-life products; the level and timing of customer design activity; the market acceptance of our customers' products; the risk that orders may not result in future revenue; our ability to introduce and produce new products based on advanced wafer technology on a timely basis; our ability to adequately market the low power, competitive pricing, and short time-to-market of our new products; intense competition; our ability to hire and retain qualified personnel; changes in product demand or supply; general economic conditions; political events, international trade disputes, natural disasters, and other business interruptions that could disrupt supply or delivery of, or demand for, the Company's products; and changes in tax rates and exposure to additional tax liabilities. These and other potential factors and uncertainties that could cause actual results to differ materially from the results contemplated or implied are described in more detail in the Company's public reports filed with the U.S. Securities and Exchange Commission (the "SEC"), including the risks discussed in the "Risk Factors" section in the Company's Annual Reports on Form 10-K, Quarterly Reports on Form 10-Q as well as disclosed in the Company's prior press releases, which are available on the Company's Investor Relations website at http://ir.quicklogic.com/, and on the SEC website at www.sec.gov/. In addition, any forward-looking statements contained herein speak only as of the date indicated and are based on management's current expectations and assumptions that we believe to be reasonable as of such date. We are not obliged to update these statements even as new information becomes available or other events occur in the future.

Non-GAAP Financial Measures

QuickLogic reports financial information in accordance with United States Generally Accepted Accounting Principles ("GAAP"), but believes that non-GAAP financial measures are helpful in evaluating its operating results and comparing its performance to comparable companies. Accordingly, the Company excludes certain charges related to stock-based compensation, impairment charges, and restructuring costs, as well as significant non-recurring gains, in calculating non-GAAP (i) operating expenses, (ii) income (loss) from operations, (iii) net income (loss), (iv) net income (loss) per share, and (v) cost of revenue and gross margin percentage. The Company provides this non-GAAP information to enable investors to evaluate its operating results in a manner consistent with how the Company analyzes its operating results and to provide consistency and comparability with similar companies in the Company's industry.

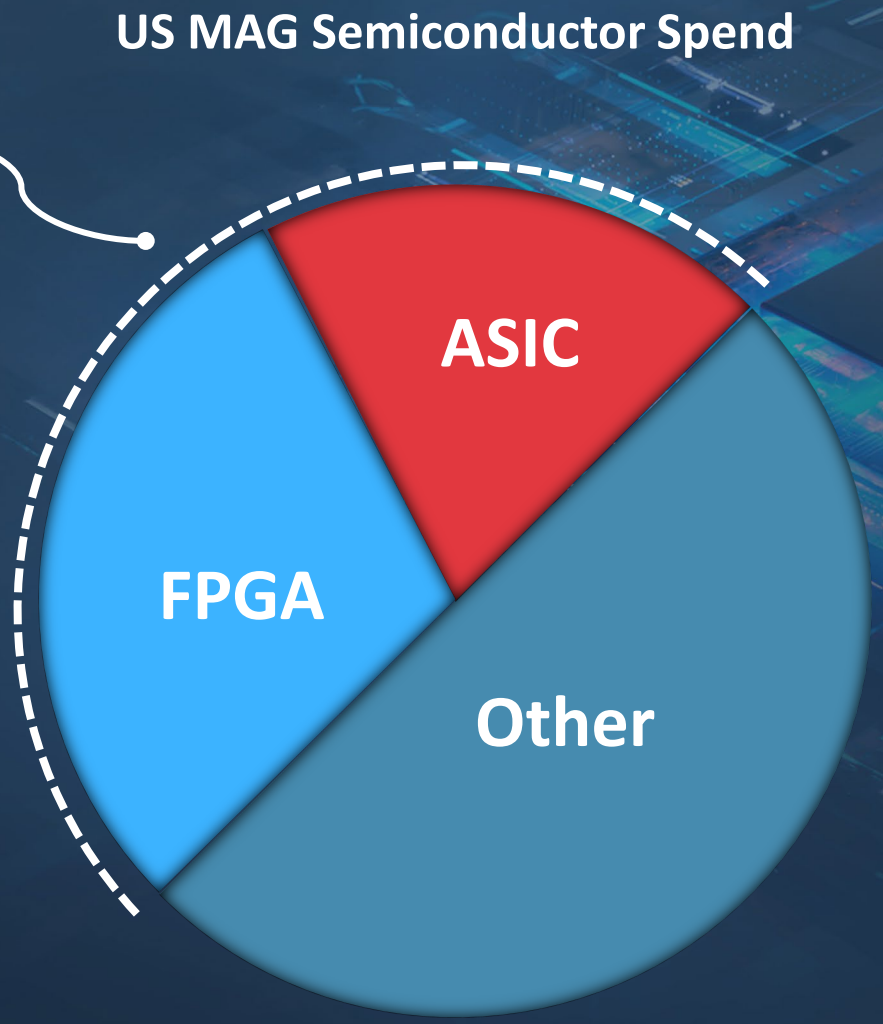
Management uses the non-GAAP measures, which exclude gains, losses, and other charges that are considered by management to be outside of the Company's core operating results, internally to evaluate its operating performance against results in prior periods and its operating plans and forecasts. In addition, the non-GAAP measures are used to plan for the Company's future periods and serve as a basis for the allocation of the Company's resources, management of operations and the measurement of profit-dependent cash flows, and equity compensation paid to employees and executive officers.

Investors should note, however, that the non-GAAP financial measures used by QuickLogic may not be the same non-GAAP financial measures and may not be calculated in the same manner as that of other companies. QuickLogic does not, nor does it suggest that investors should, consider such non-GAAP financial measures alone or as a substitute for financial information prepared in accordance with GAAP. A reconciliation of GAAP financial measures to non-GAAP financial measures is included in the financial statements portion of this press release. Investors are encouraged to review the related GAAP financial measures and the reconciliation of non-GAAP financial measures with their most directly comparable GAAP financial measures.

QuickLogic uses its website, the company blog, and LinkedIn page as channels of distribution of information about its products, its planned financial and other announcements, its attendance at upcoming investor and industry conferences, and other matters. Such information may be deemed material information, and QuickLogic may use these channels to comply with its disclosure obligations under Regulation FD. Therefore, investors should monitor the Company's website and its social media accounts in addition to following the Company's press releases, SEC filings, public conference calls, and webcasts.

QuickLogic® Riding the Renaissance of Programmable Logic

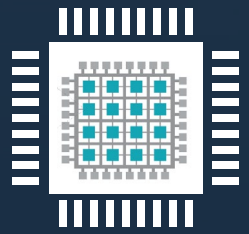
- eFPGA IP and ruggedized FPGA products target a >$1B served available market
- FPGAs account for >30% of U.S. Military, Aerospace & Government (MAG) semiconductor spend
- FPGA market expected to grow at >16.4% (1) CAGR from 2024–2029, driven by MAG
- Accelerating Storefront and IP diversification leading to a financial inflection ahead



(1) Markets and Markets

Adaptable Hardware: Discrete FPGA & Embedded FPGA

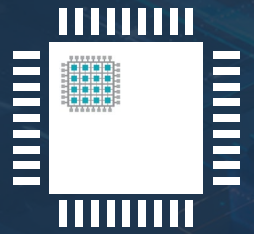
Discrete FPGA



Standalone programmable devices

System-level adaptability w/o board redesign

Embedded FPGA Hard IP



Licensable IP for customer ASICs and SoCs

Device-level adaptability w/o ASIC spin

Benefits of Reconfigurability

Adapt algorithms, security, and interfaces *after deployment* to support evolving requirements — extending lifecycles, lowering power, and improving performance

FPGA: Adaptable Technology for Mission-Critical Systems

Market Drivers

- Defense modernization
- Space & satellites
- Uncrewed Systems (UAS & UAV)
- Automotive / ADAS
- Edge AI & computing
- Industrial automation & robotics
- Secure U.S. semiconductor supply

Why FPGA

- Reprogrammability
- Real-time processing
- Low latency
- Parallel processing
- Long lifecycle
- SWaP-C optimization

Critical Applications

- Radar & electronic warfare
- Flight control & guidance
- Secure communications
- Avionics & space systems
- Image & signal processing
- Motion control & networking

MARKET NEED → PROVEN HARDWARE → MISSION-CRITICAL APPLICATIONS







Delivering Complete eFPGA IP and Ruggedized FPGAs



- Founded in 1988
- Headquartered in San Jose, CA
- Nasdaq listed since 1999 - QUIK
- Uniquely positioned for growth

SWaP-C: size, weight, power and cost

Vertical Technology Stack Supports New Business Wins

Storefront Devices/Chiplets

- Commercial off-the-shelf FPGAs and chiplets provide recurring device revenue
- Optimized for cost and time-to-market

eFPGA IP

- Delivered across 13+ nodes; each new node unlocks incremental, high-margin follow-on contracts
- Leverages more than three decades of development

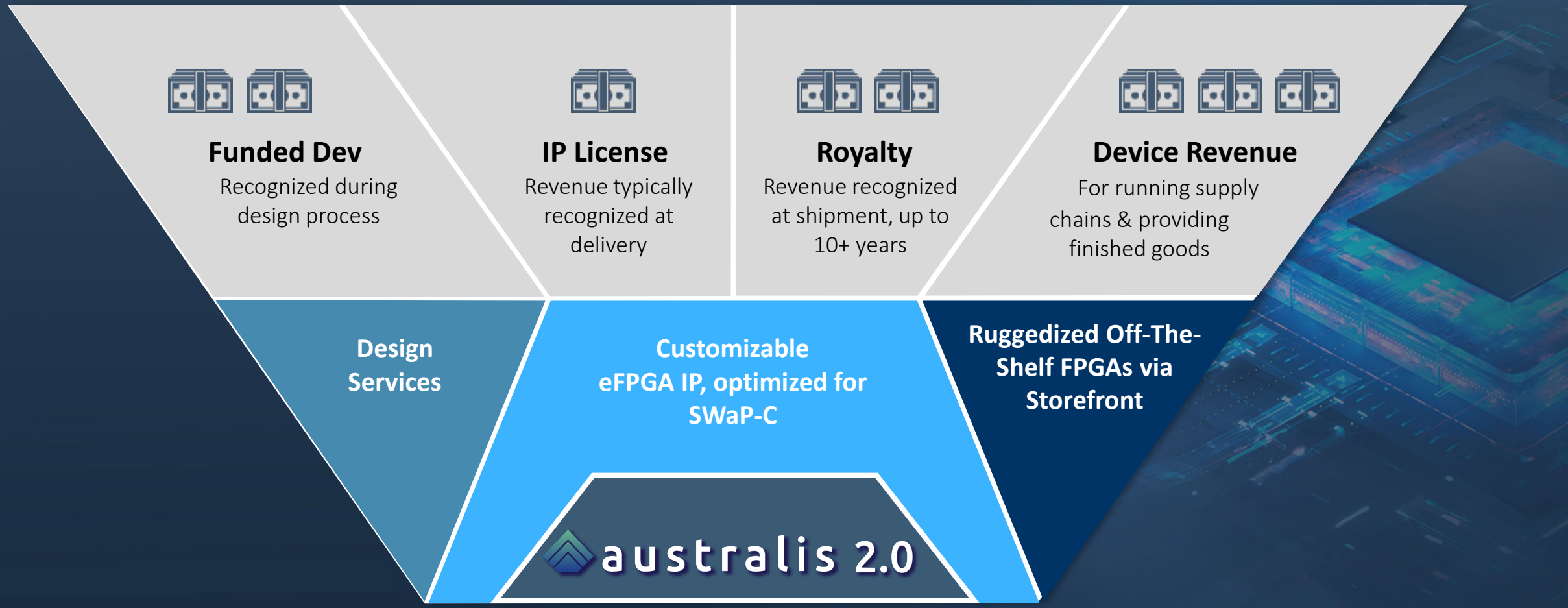
Australis™ Tool

- Proprietary eFPGA IP generator that compresses development
- Enables rapid targeting of any foundry or process node

FPGA Expertise

- 50 million devices shipped to 2,000 customers
- 13 fabrication nodes qualified

Services and Storefront Broaden Market for eFPGA IP



SWaP-C: size, weight, power and cost

Aerospace & Defense Driven Opportunities

USG Contract — Strategic Rad-Hard FPGA

Awarded contract to develop Rad-Hard FPGA technology for DoD strategic and space systems — options now total $89M

First five tranches (~$48M) awarded to date;
Two tape-outs planned in early 2027

Internally Funded — Strategic Rad-Hard FPGA (GF12LP)

RadPro™ Test chips received Q1 2026;
Dev Kit shipments began Q2;
Only source for a US-fabricated SRH FPGA

Targets DoW Strategic Defense and Satellite Systems; separate from USG Contract

EVERSPIN TECHNOLOGIES · GlobalFoundries · Honeywell · skywater · TRUSTED Semiconductor Solutions

Strategic Rad-Hard FPGAs for Defense Industrial Base – No Existing FPGA Alternatives

Modernization of Strategic Defense Systems Has Influenced Investment in Strategic Rad-Hard FPGAs

- U.S. Domiciled
- U.S. Manufacturing Operations
- U.S. Personnel
- Highest Levels of Radiation Hardness



New Market Opportunities Representing

>$100M*

* Based on Management's estimates

Defense Semiconductor Revenue Lifecycle

ILLUSTRATIVE FRAMEWORK

Potential QuickLogic revenue progression from funded development through scaled Program of Record deployment

PROGRAM START — VALIDATED SILICON — INITIAL PRODUCTION — SCALED DEPLOYMENT

1 Funded Development and Qualification

Illustrative timing: Years 0–4

- Prototype or test-chip tape-out
- Engineering Samples / Dev Kit shipments

MILLIONS OF DOLLARS
NRE, IP, tools and prototypes

2 Initial Program Production

Illustrative timing: Years 3–4

- Customer system integration
- Initial procurement lots

POTENTIAL LOW TENS OF MILLIONS
Low-Rate Initial Production (LRIP) and support revenue

3 Scaled Program Deployment

Illustrative timing: Year 4+

- Sustaining and lifecycle support
- Potential multiple sockets per system

POTENTIAL RECURRING REVENUE
Full-Rate Production (RFP) Driven by scale, content and duration

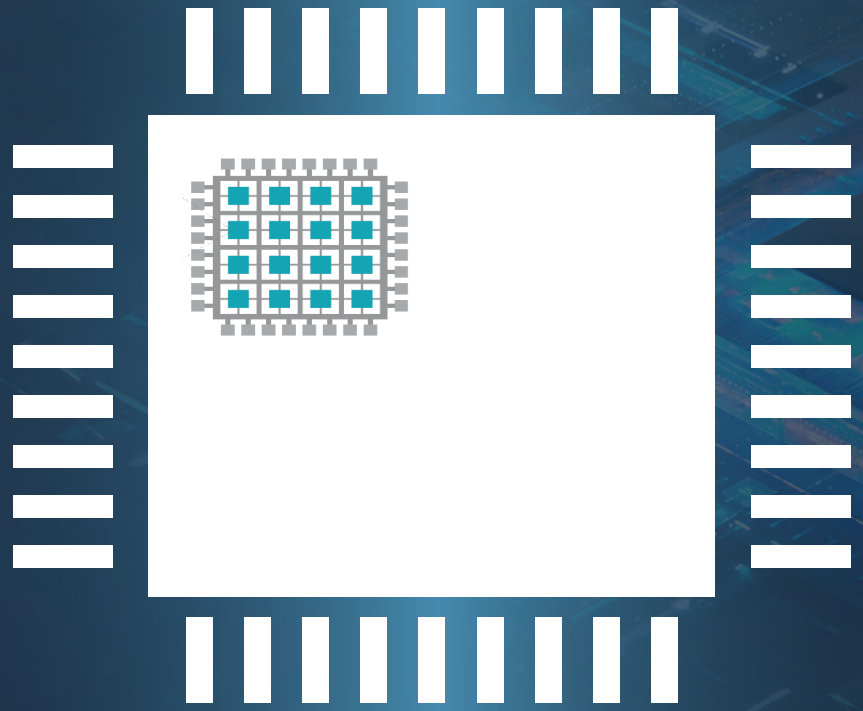
INVESTOR TAKEAWAY

Successful defense programs can progress from development revenue to recurring production revenue over long deployment lifecycles.

Illustrative only. Timing and revenue potential vary by program and depend on technical success, qualification, funding, procurement decisions, quantities, pricing and QuickLogic's commercial role. Not company guidance or a customer commitment.

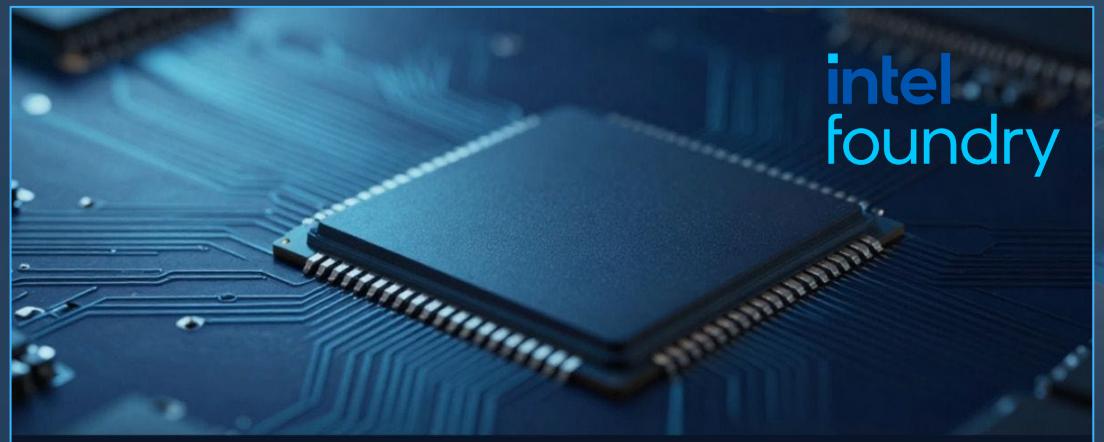
eFPGA IP Core Portfolio Expansion

- 10+ eFPGA IP Licensees
- 15 Australis-Generated eFPGA IPs
- Available Process Nodes
  - » GlobalFoundries®: 12LP, 12LP SRHBD, 22FDX®
  - » Intel®: 18A and 18A-P (target Q4’26) Technology
  - » TSMC: 12e, 12FFC
  - » UMC: 22



Ecosystem and IP Collaborations

- Member of Intel Foundry Accelerator ecosystem, including Chiplet, IP and USMAG alliances
- Security Solutions for HW Root of Trust and PQC (Post-Quantum Cryptography) through ecosystem collaborations
- YorChip collaboration for UCIe chiplets targeting Edge AI/ML and IoT
- Introduced first phase of Digital Proof of Concept Chiplet program prior to customer commitments



QuickLogic Joins the Intel Foundry Accelerator Program, including Chiplet, IP, and USMAG Alliances

An Expanding Customer Base and Ecosystem

BAE SYSTEMS | cadence | chipus | DARPA | EPSON | EVERSPIN TECHNOLOGIES

FARADAY | flex | GlobalFoundries | Google | Honeywell | IDAHO SCIENTIFIC

intel foundry | KYOCERA | SAMSUNG | skywater | STARR-Lab Semiconductor Technology And Rad-Effects Research Lab | SYNOPSYS

TRUSTED Semiconductor Solutions | tsmc | UMC | U.S. Department of War | XIPHERA | YorChip

Defense

Work with all of the Top 5 and 8 of the top 10 DoD prime contractors

BAE Systems' Supplier of the Year Honors

Named BAE Systems "FAST Labs™ Technology Innovation Partner of the Year"

Recognition underscores QuickLogic's leadership in delivering state-of-the-art embedded FPGA Hard IP to the Defense Industrial Base



Received May 2024

eFPGA IP Traction: Contracts, Technology & Expanding Nodes



Contract Momentum

- $65M+ in new eFPGA contracts since July 2021, with significant pipeline upside across six process nodes, chiplets, and Storefront
- Includes $1M eFPGA Hard IP for Data Center ASIC and contract to expand IP cores up to 1M LUT
- Six-figure eFPGA Hard IP win in automotive and industrial robotics sectors

Australis: The Game Changer

- Proprietary IP generator compresses eFPGA development from >1 year to months, dramatically reducing cost and accelerating customer time-to-market.
- Australis 2.0 targets advanced nodes for faster core speeds, improved silicon utilization, and high-reliability features.

Process Node Breadth

- 13 qualified nodes, from 250nm to Intel 18A / Intel 18A-P
- Each new node unlocks new customers; follow-on contracts execute in weeks/months with improved margins

Australis 2.0 eFPGA IP Generator — The Game Changer

Weeks	13+	$65M+
vs. >1 year the old way	Fabrication nodes supported	Contracts won on Australis IP

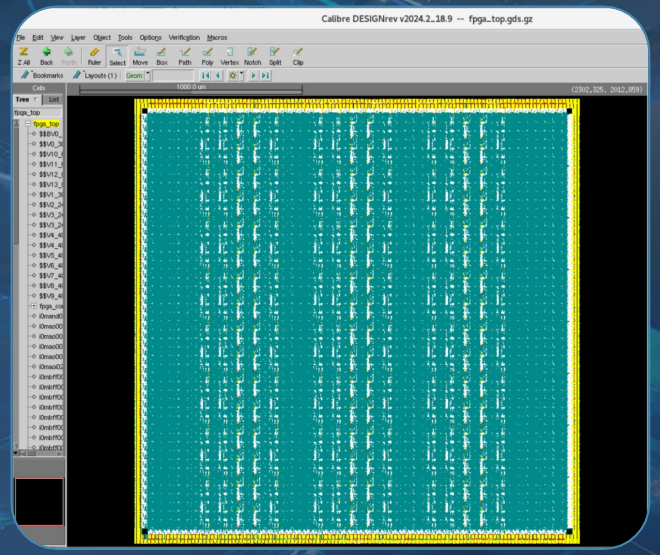
NEW WAY	OLD WAY
Automated generation Increases Engineering design capacity	**Large architect team** Manual, resource-heavy
Days / Weeks / Months Off the shelf / custom array / new process	**>1+ years per design** Delays every program
Reusable across nodes Follow-ons execute in weeks	**Node-locked** Restarts from scratch each time

Works with GlobalFoundries™ intel foundry SAMSUNG skywater tsmc UMC

Silicon-Proven Onshore eFPGA IP

QuickLogic is the only company to deliver eFPGA Hard IP on a sub-5nm process node

- First eFPGA IP on Intel 18A / Intel 18A-P technology
- GF12LP, GF22FDX and more…
- Multiple active customers



Intel 18A / Intel 18A-P

Most advanced node with eFPGA Hard IP — ever

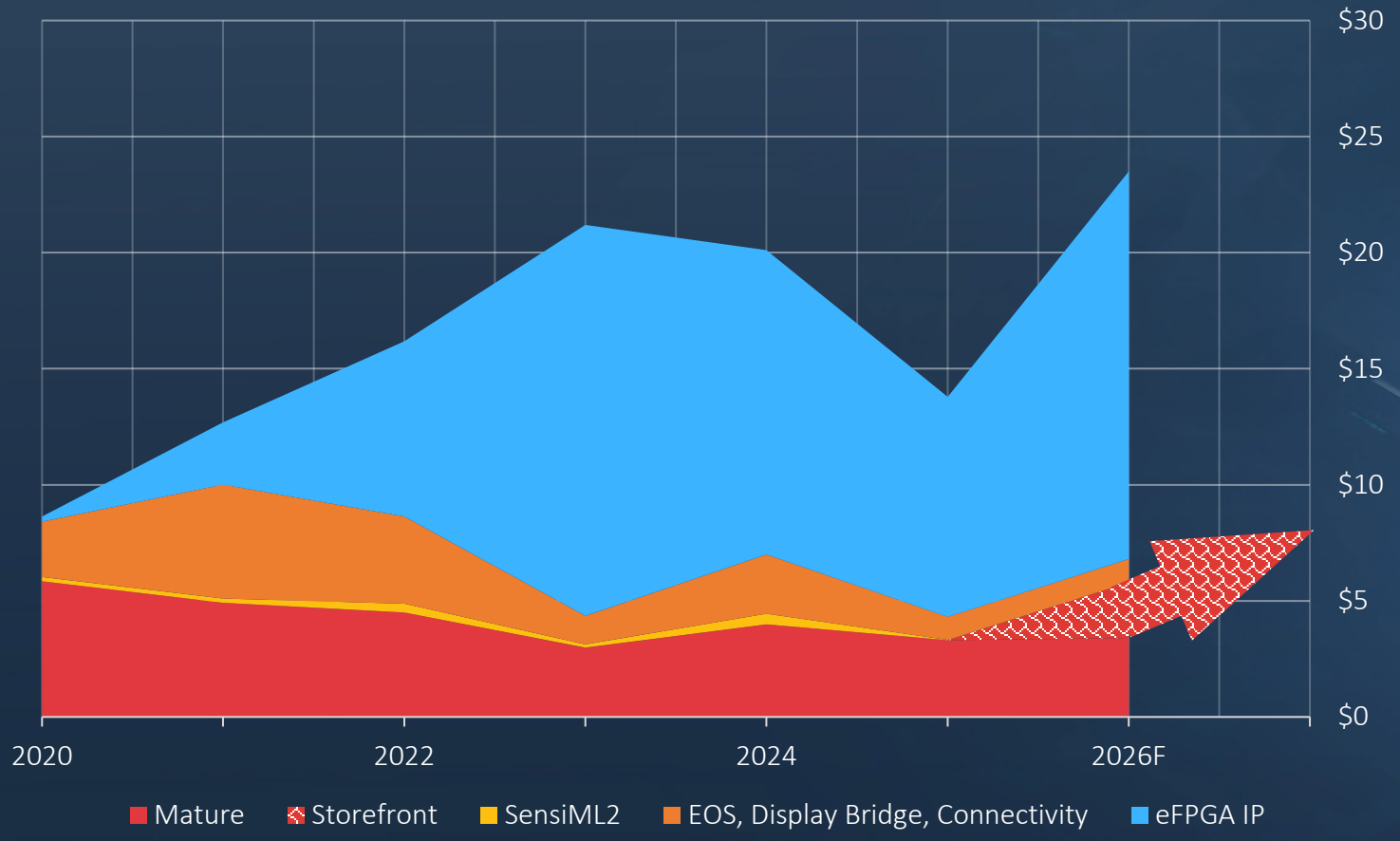
GF 12LP, GF22FDX

Broadly used across DIB

Now

Available for commercial & Defense customers

eFPGA and Storefront Opportunities to Drive Growth



2025

- 10% 5-YR revenue CAGR
- eFPGA representing 69% of total revenue
- Reflects expected lower DoD contract revenue recognition

2026

- Total revenue to grow 70%+
- Rad-Hard Dev Kits and test chips begin revenue in 2026

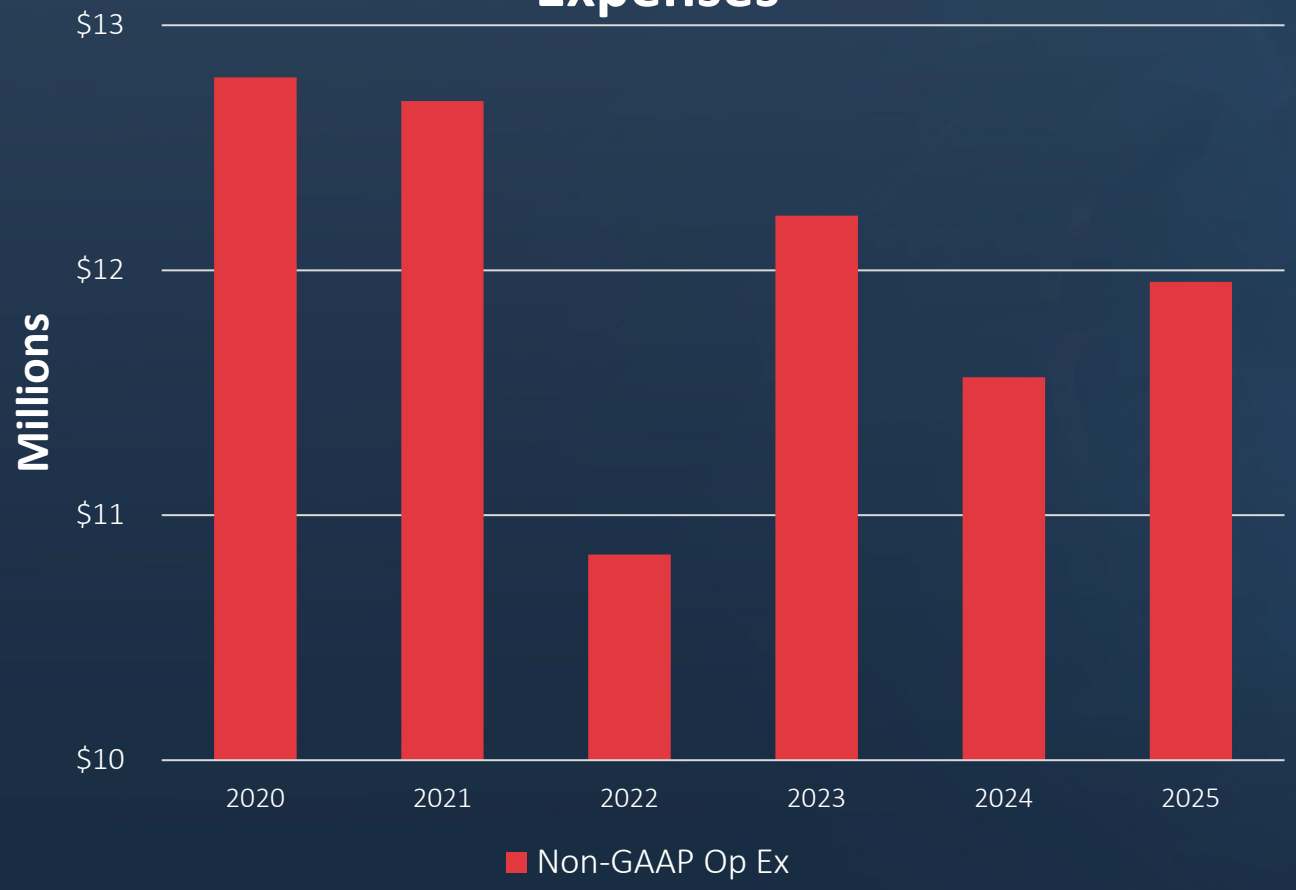
2027 and Beyond

- Material growth in Storefront revenue expected as QUIK's SRH FPGA is designed into strategic defense systems
- Each "socket" could represent $10Ms to $100Ms

These outlook figures are based on management's current expectations and assumptions are subject to risks and uncertainties. For more information, please review the Company's public filings as well as the material included in the Appendix to this presentation. Please refer to Appendix for Non-GAAP to GAAP reconciliation

Our Model Enables High Operating Leverage

Disciplined Control of Operating Expenses



Operating Leverage is Non-GAAP Gross Profit $ divided by Non-GAAP OpEx

As more designs move to License & Royalty and/or Storefront Device shipments, Gross Profit $ increases faster than OpEx

2026F for Non-GAAP Op Ex is $13.3-$13.4M – up 12% on estimated revenue growth of 72%

Driving 70+% 2026 Revenue Growth and Second Half Profitability

1

Defense Storefront Acceleration

$89M SRH contract execution; Internally-funded SRH FPGA Dev Kits shipping — each socket win represents long-term device revenue

2

eFPGA IP Diversification

Non-SRH eFPGA revenue growing; Intel 18A delivery complete; Pipeline spans 13 nodes, data center, and chiplet markets

3

Financial Inflection Ahead

Over 70% revenue growth forecast for 2026 with measured growth in non-GAAP OpEx to drive positive cash flow and non-GAAP net income in the second half









These outlook figures are based on management's current expectations and assumptions are subject to risks and uncertainties. For more information, please review the Company's public filings as well as the material included in the Appendix to this presentation. Please refer to Appendix for Non-GAAP to GAAP reconciliation

QuickLogic

Thank you!



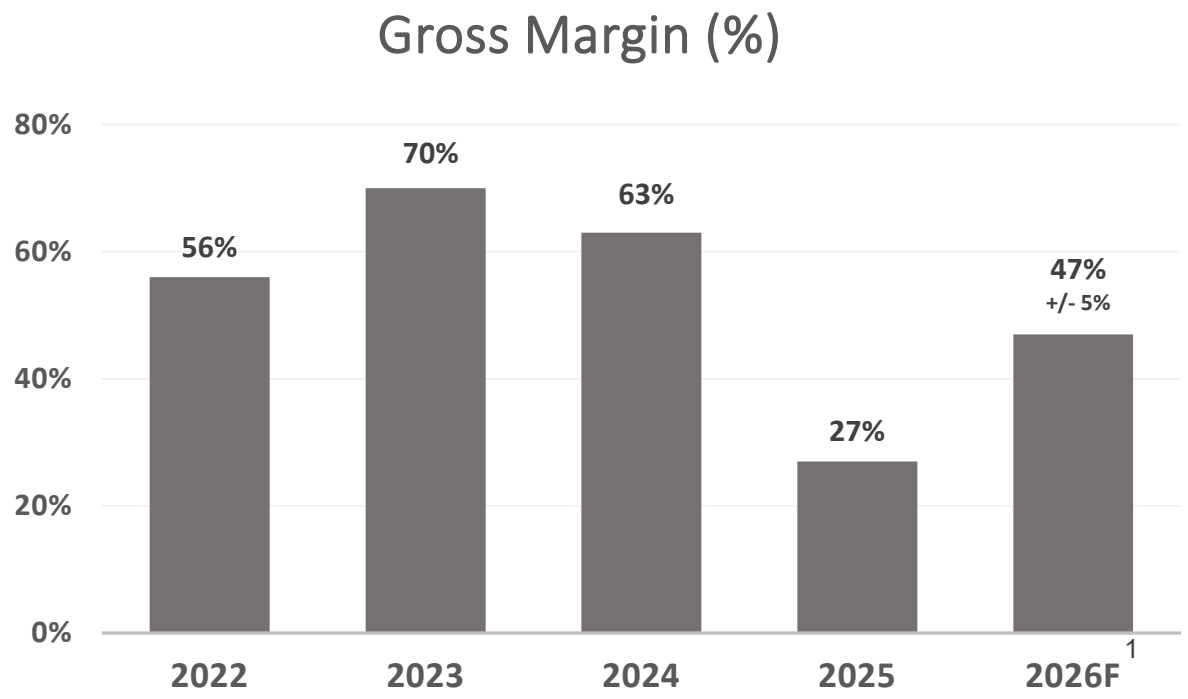
Appendix



Revenue & Gross Margin Trends

Supporting Rising Margins (Non-GAAP)



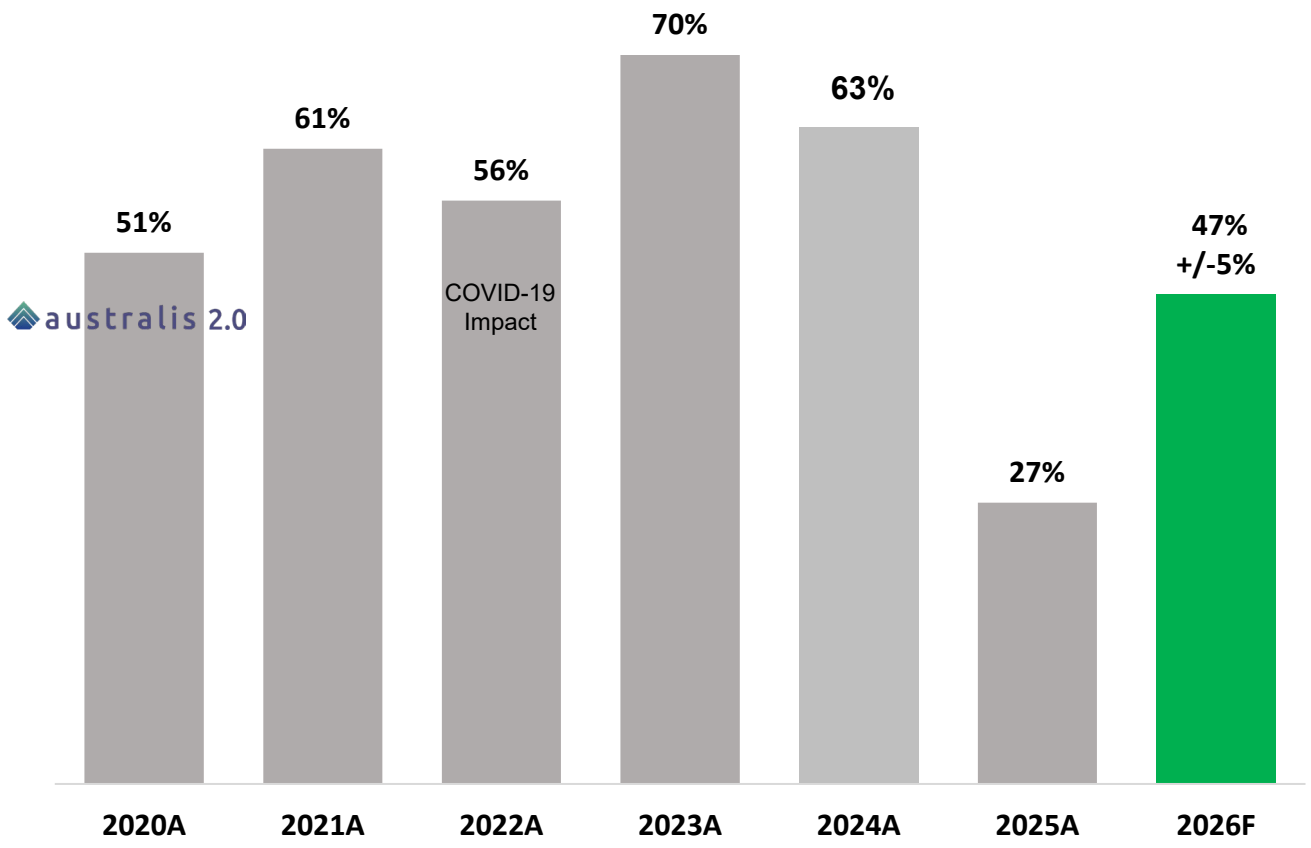


1. 2026 revenue guidance is midpoint and gross margin is estimated and based on Company projections provided November 10, 2026 earnings call, as well as expected trends in the business. These outlook figures are based on management's current expectations, and assumptions are subject to risks and uncertainties. For more information, please review the Company's public filings, as well as the material included in the Appendix to this presentation.

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Transforming Financial Model

Non-GAAP Gross Margin(1)



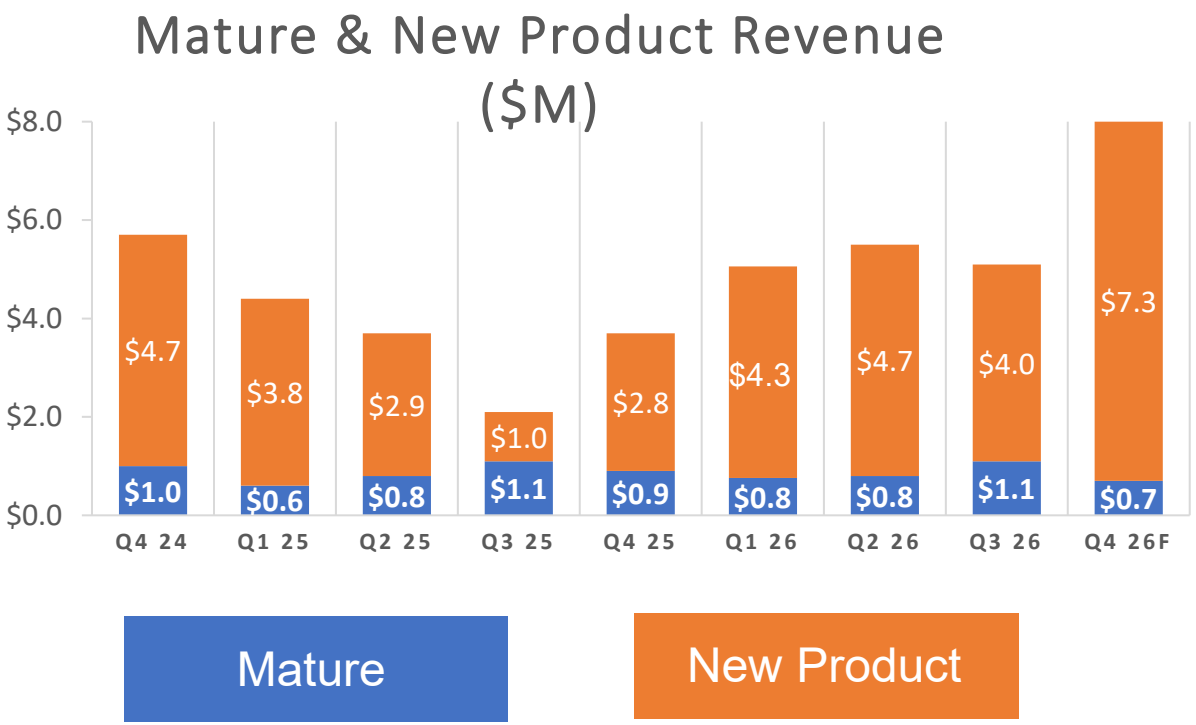
Driving Net Income (Non-GAAP)

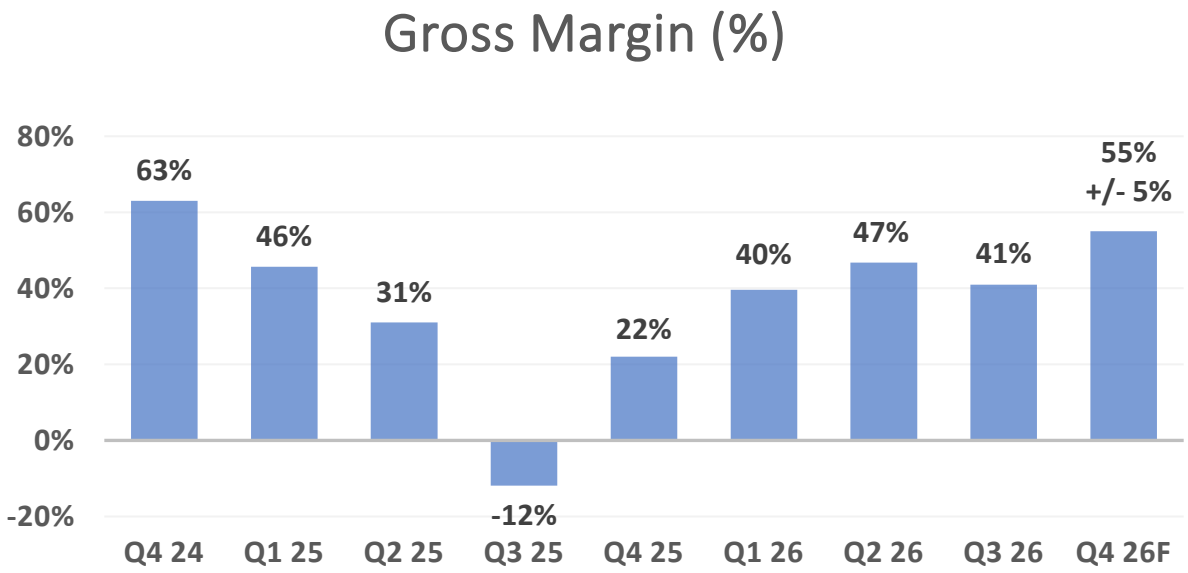
1. eFPGA, software and licensing opportunities driving long-term revenue growth
2. Australis enabling expansion of addressable process technologies and providing future leverage for investments made
3. Optimized expense structure to support operating model
4. Non-GAAP net income and positive cash flow expected in 2H FY'26

(1) Please refer to Appendix for Non-GAAP to GAAP reconciliation.

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Trended Quarterly Financial Summary (Non-GAAP)

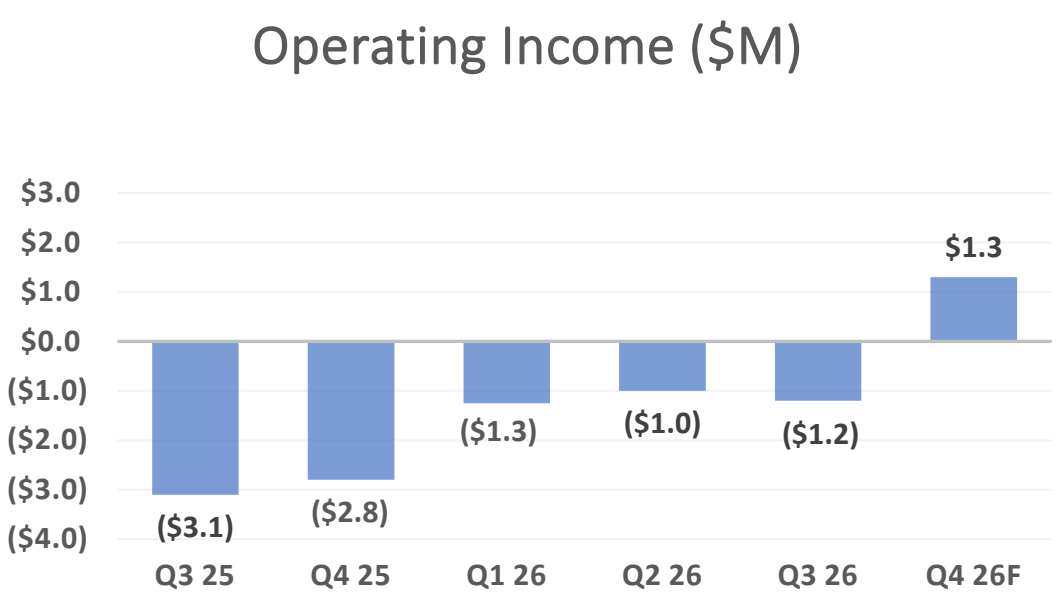


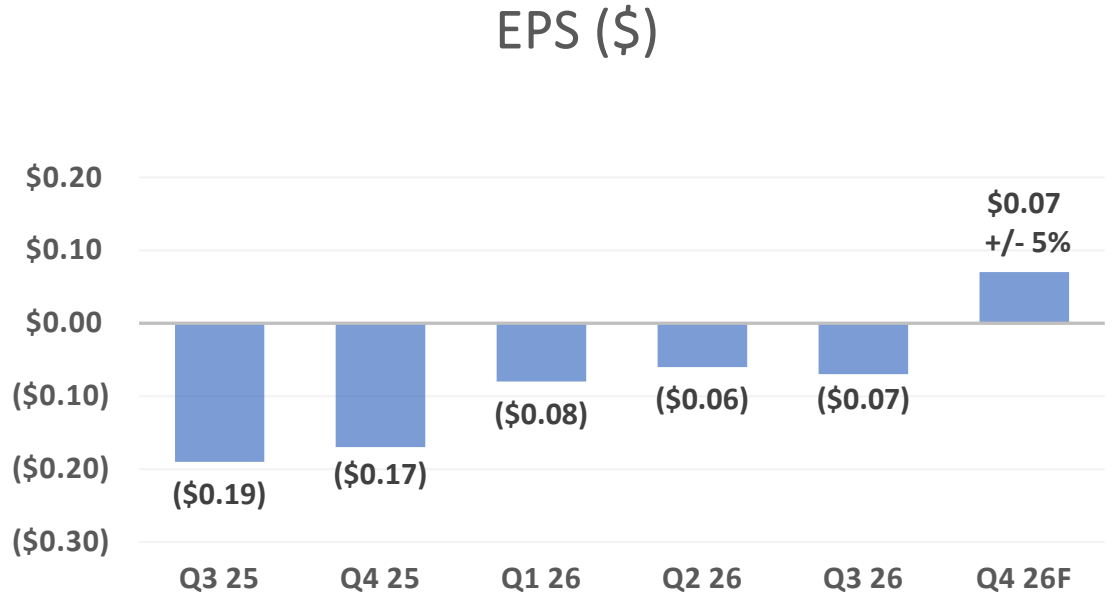


(F) Midpoint of forecasts for Q4 FY'26 based on Company projections, as announced on the November 10, 2026 earnings call

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2025 Investments Should Return Business to Net Income (Non-GAAP) in 2H 2026





Midpoint of forecasts for Q4 FY'26 based on Company projections, as announced on November 10, 2026 earnings call. Forecasted EPS assumes shares outstanding of 19.0M for Q4'26.

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Non-GAAP Quarterly Income Statements

($ in millions, except per share data)	Q3'2024	Q4'2024	Q1'2025	Q2'2025	Q3'2025	Q4'2025	Q1'2026	Q2'2026	Q3'2026
New product	$ 3.5	$ 4.7	$ 3.8	$ 2.9	$ 1.0	$ 2.8	$ 4.3	$ 4.7	$ 4.0
Mature product	0.7	1.0	0.6	0.8	1.1	0.9	0.8	0.8	1.1
Total Revenue	**$ 4.3**	**$ 5.7**	**$ 4.3**	**$ 3.7**	**$ 2.0**	**$ 3.7**	**$ 5.1**	**$ 5.5**	**$ 5.1**
Gross margin %	61%	63%	46%	31%	-12%	22%	40%	47%	41%
Operating expense									
Research and development	$ 1.6	$ 1.4	$ 1.1	$ 1.0	$ 1.3	$ 1.3	$ 1.3	$ 1.4	$ 1.4
Selling, general and administrative	1.7	1.5	1.9	1.5	1.6	2.3	1.9	2.1	1.9
Total operating expenses	**$ 3.3**	**$ 2.9**	**$ 3.0**	**$ 2.5**	**$ 2.9**	**$ 3.6**	**$ 3.3**	**$ 3.5**	**$ 3.3**
Income (loss) from operations	**$ (0.7)**	**$ 0.7**	**$ (1.0)**	**$ (1.4)**	**$ (3.1)**	**$ (2.8)**	**$ (1.3)**	**$ (1.0)**	**$ (1.2)**
Net Income (loss)	**$ (0.9)**	**$ 0.6**	**$ (1.1)**	**$ (1.5)**	**$ (3.2)**	**$ (2.8)**	**$ (1.3)**	**$ (1.1)**	**$ (1.3)**
Net income (loss) per share	**$ (0.06)**	**$ 0.04**	**$ (0.07)**	**$ (0.09)**	**$ (0.19)**	**$ (0.17)**	**$ (0.08)**	**$ (0.06)**	**$ (0.07)**

Source: Company SEC filings. Note: Totals may not equal due to rounding.

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Non-GAAP to GAAP Reconciliation

($ in thousands, except per share data)	Q3'2024	Q4'2024	FY2024	Q1'2025	Q2'2025	Q3'2025	Q4'2025	FY2025	Q1'2026	Q2'2026	Q3'2026
Non-GAAP operating income (loss)	**$ (656)**	**$ 698**	**$ 1,175**	**$ (1,037)**	**$ (1,367)**	**$ (3,102)**	**$ (2,758)**	**$ (8,264)**	**$ (1,253)**	**$ (951)**	**$ (1,197)**
Adjustment for stock-based compensation	(1,205)	(924)	(4,606)	(904)	(843)	(828)	(744)	(3,319)	(858)	(754)	(890)
Adjustment for impairment charges	-	-	-	-	(300)	-	(2,355)	(2,655)	-	-	-
Adjustment for restructuring costs	-	-	-	(141)	(21)	-	-	(162)	(11)	(16)	(34)
GAAP operating income (loss)	**$ (1,861)**	**$ (226)**	**$ (3,431)**	**$ (2,082)**	**$ (2,531)**	**$ (3,930)**	**$ (5,857)**	**$ (14,400)**	**$ (2,122)**	**$ (1,721)**	**$ (2,121)**
Non-GAAP net income (loss)	**$ (889)**	**$ 619**	**$ 765**	**$ (1,146)**	**$ (1,506)**	**$ (3,179)**	**$ (2,849)**	**$ (8,680)**	**$ (1,337)**	**$ (1,067)**	**$ (1,259)**
Adjustment for stock-based compensation	(1,205)	(924)	(4,606)	(904)	(843)	(828)	(744)	(3,319)	(858)	(754)	(890)
Adjustment for impairment charges	-	-	-	-	(300)	-	(2,355)	(2,655)	-	-	-
Adjustment for significant non-recurring gain	-	-	-	-	-	-	-	-	-	950	-
Adjustment for restructuring costs	-	-	-	(141)	(21)	-	-	(162)	(11)	(16)	(34)
GAAP net income (loss)	**$ (2,094)**	**$ (305)**	**$ (3,841)**	**$ (2,191)**	**$ (2,670)**	**$ (4,007)**	**$ (5,948)**	**$ (14,816)**	**$ (2,206)**	**$ (887)**	**$ (2,183)**
Non- GAAP net income (loss) per share	**$ (0.06)**	**$ 0.04**	**$ 0.05**	**$ (0.07)**	**$ (0.09)**	**$ (0.19)**	**$ (0.17)**	**$ (0.53)**	**$ (0.08)**	**$ (0.06)**	**$ (0.07)**
Adjustment for stock-based compensation	(0.08)	(0.06)	(0.31)	(0.06)	(0.06)	(0.05)	(0.04)	(0.21)	(0.05)	(0.04)	(0.05)
Adjustment for impairment charges	-	-	-	-	(0.02)	-	(0.14)	(0.16)	-	-	-
Adjustment for significant non-recurring gain	-	-	-	-	-	-	-	-	-	0.05	-
Adjustment for restructuring costs	-	-	-	(0.01)	-	-	-	(0.01)	-	-	-
GAAP net income (loss) per share	**$ (0.14)**	**$ (0.02)**	**$ (0.26)**	**$ (0.14)**	**$ (0.17)**	**$ (0.24)**	**$ (0.35)**	**$ (0.91)**	**$ (0.13)**	**$ (0.05)**	**$ (0.12)**
Non-GAAP gross margin %	**61.4%**	**62.9%**	**63.3%**	**45.7%**	**31.0%**	**-11.9%**	**22.4%**	**27.0%**	**39.6%**	**46.8%**	**41.0%**
Adjustment for stock-based compensation	(5.6)%	(3.1)%	(4.2)%	(2.2)%	(5.1)%	(11.4)%	(4.3)%	(4.9)%	(3.1)%	(2.9)%	(3.6)%
GAAP gross margin %	**55.8%**	**59.8%**	**59.1%**	**43.5%**	**25.9%**	**-23.3%**	**18.1%**	**22.1%**	**36.5%**	**43.9%**	**37.4%**

Source: Company SEC filings.

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Balance Sheet and Capitalization Summary

Balance Sheet Summary as of September 27, 2026*

($ in thousands)

Assets		
Current assets		
Cash and cash equivalents	$	19,199
Accounts receivables		1,030
Contract assets		335
Inventories		938
Other current assets		2,308
Total current assets	**$**	**23,810**
Fixed Assets & Other		18,428
TOTAL ASSETS	**$**	**42,238**
Liabilities		
Current liabilities		
Revolving line of credit	$	5,000
Trade payables		2,235
Accrued liabilities		1,732
Deferred revenue		786
Lease liabilities, current		218
Notes payable, current		1,466
Total current liabilities	**$**	**11,437**
Notes payable		823
TOTAL LIABILITIES	**$**	**12,260**
TOTAL STOCKHOLDERS' EQUITY	**$**	**29,978**
TOTAL LIABILITIES AND STOCKHOLDERS' EQUITY	**$**	**42,238**

Capitalization as of September 27, 2026

Debt	Interest Rate	Maturity	Amount
Revolving Credit Line Facility - Sunflower Bank	7.50%	Apr. 24, 2029	$10M
Revolving Credit Line - Sunflower Bank - borrowing in Q3'26			$5M

Equity Awards Outstanding (in thousands of shares)	Amount
Restricted Stock Units	**347**
Common Stock Outstanding	**18,754**

* Unaudited

Source: Company SEC filings.

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