

July 18, 2019



QuickLogic to Participate in the Oppenheimer 22nd Annual Technology, Internet & Communications Conference

SAN JOSE, Calif., July 18, 2019 /PRNewswire/ -- QuickLogic Corporation (NASDAQ: QUIK), developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, and Endpoint AI solutions, announced that members of its executive management team are scheduled to participate in the Oppenheimer 22nd Annual Technology, Internet & Communications Conference on Wednesday, August 7, 2019.

Event:	Oppenheimer 22nd Annual Technology, Internet & Communications Conference
Date:	Wednesday, August 7, 2019
Location:	Four Seasons Hotel – Boston
Presentation Time:	1:05 p.m. Eastern Time



A webcast of the presentation will be available in QuickLogic's IR Site [Events Page](#). QuickLogic management will be available for one-on-one meetings with investors. Interested investors should contact their Oppenheimer representative, or QuickLogic Investor Relations at ir@quicklogic.com, to secure a meeting time.

About QuickLogic

QuickLogic develops low power, multi-core semiconductor platforms and Intellectual Property (IP) for Artificial Intelligence (AI), voice and sensor processing. The solutions include an embedded FPGA IP (eFPGA) for hardware acceleration and pre-processing, and heterogeneous multi-core SoCs that integrate eFPGA with other processors and peripherals. The Analytics Toolkit from the company's wholly-owned subsidiary, SensiML completes the end-to-end solution with accurate sensor algorithms using AI technology. The full range of platforms, software tools and eFPGA IP enables the practical and efficient adoption of AI, voice and sensor processing across the multitude of mobile, wearable, hearable, consumer, industrial, edge and endpoint IoT applications. For more information, visit <http://www.quicklogic.com> and <https://www.quicklogic.com/blog/>.

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